

Connectors

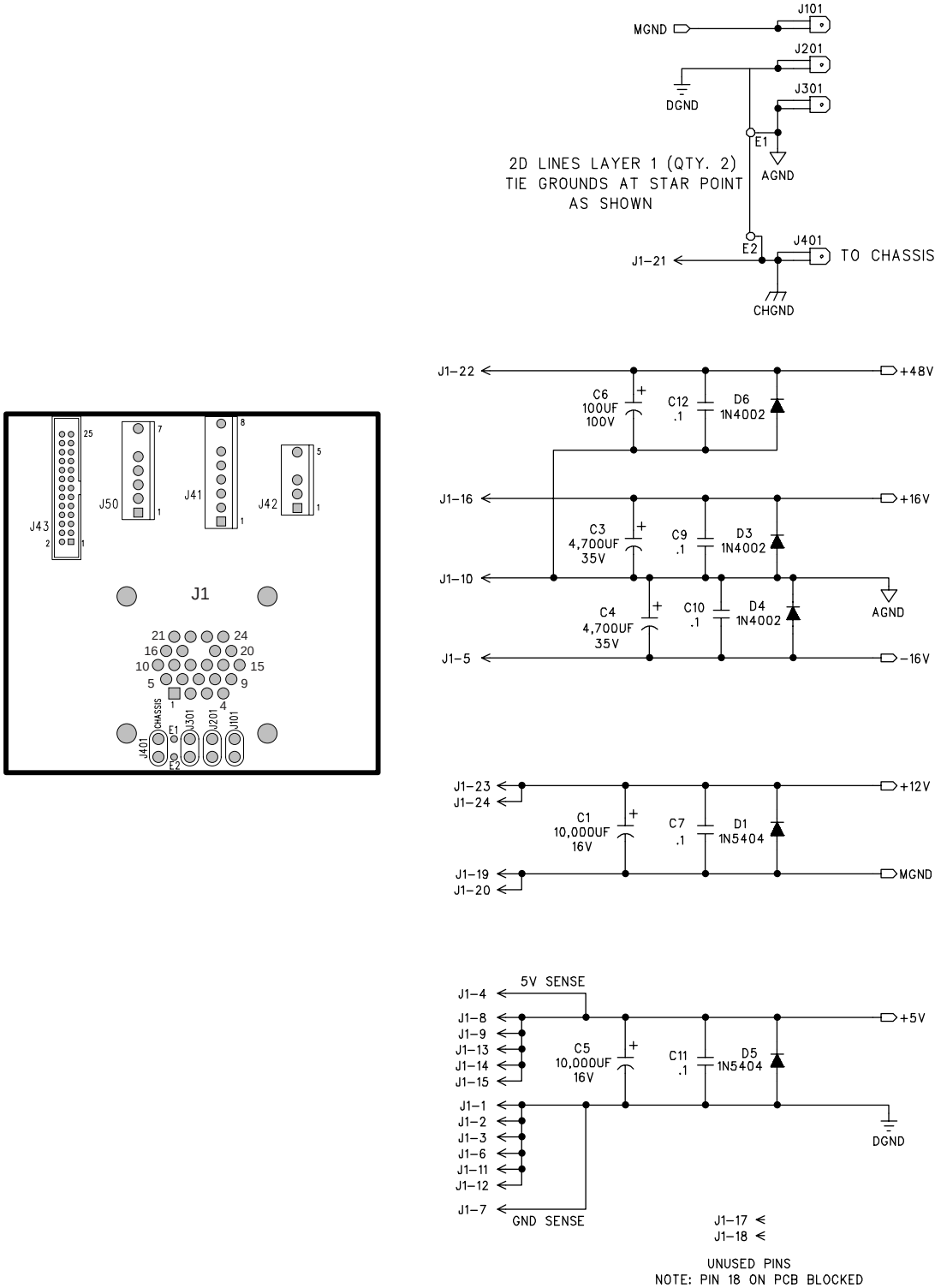
This table shows each connector and the boards they connect to. Note that the connectors on each board are joined to the same numbered connector on other boards. For example, J4 on the Analog I/O board connects to J4 on the Mic/Line control surface.

The following pages show each connector in detail, including the labeling and designation on each pin on either end of the connecting cable.

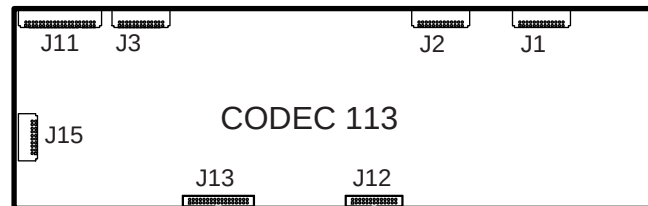
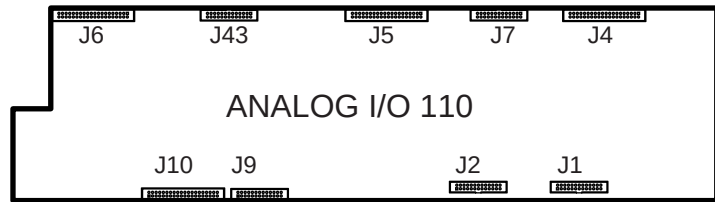
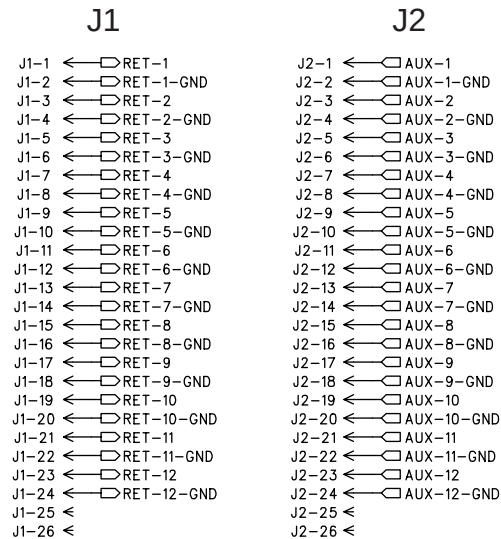
J #	Pins	From	To	Description	Page
J1	24...	111 Power distribution	Remote CPU p/s	Incoming power	2
J1	26...	110 Analog I/O	113 Codec	Returns	3
J2	26...	110 Analog I/O	113 Codec	Auxs	3
J3	26...	121 Line ctrl surface	113 Codec	Line13-24	4
J4	40...	110 Analog I/O	120 Mic/Line ctrl surface	INPUT Mic/Line1-6	5
J5	40...	110 Analog I/O	120 Mic/Line ctrl surface	INPUT Mic/Line7-12	5
J6	40...	110 Analog I/O	121 Line ctrl surface	INPUT Line13-24	6
J7	26...	110 Analog I/O	120 Mic/Line ctrl surface	Send1-12	5
J8	26...	112 DCA	8 BUS OUT (D Connector)	Buses Out	7
J9	26...	110 Analog I/O	112 DCA	Phones/Studio	7
J10	40...	110 Analog I/O	112 DCA	Main Mix L/R and more	8
J11	40...	112 DCA	113 Codec	Bus1-8, MixL/R, MeterL/R, SoloL/R	9
J12	26...	114 DSP	113 Codec	DT9-DT12, DR9-DR12	10
J13	34...	114 DSP	113 Codec	DT1-DT8, DR1-DR8	10
J14	62...	163 Backplane	Effects card 3	Effects card slot 3	12
J15	20...	163 Backplane	113 Codec	Clocks to Codec	11
J16	62...	163 Backplane	Effects card 2	Effects card slot 2	12
J17	62...	163 Backplane	Effects card 4	Effects card slot 4	12
J18	62...	163 Backplane	119 Tape I/O card	Tape I/O card slot 17-24	13
J19	62...	163 Backplane	119 Tape I/O card	Tape I/O card slot 9-16	13
J20	62...	163 Backplane	119 Tape I/O card	Tape I/O card slot 17-24	13
J21	20...	163 Backplane	114 DSP	Clock, Effects and Digital I/O data	14
J22	62...	163 Backplane	Alternate I/O card	Extra slot for I/O	15
J23	62...	163 Backplane	164 Clock Card	Clock Card Slot	16
J24	34...	163 Backplane	114 DSP	Effects data	14
J25	10...	114 DSP	Data In	From remote CPU (D Connector)	17
J26	10...	136 Brain	Data In	From remote CPU (D Connector)	18
J27	14...	136 Brain	VF Display	Connection to VFD	18
J28	26...	136 Brain	109 Meter	Connection to Meter	19
J29	14...	109 Meter	108 Meter	Meter join	20
J30	62...	163 Backplane	115 Digital I/O card	Digital I/O card slot	21
J31	20...	136 Brain	112 DCA	Computer control	22
J32	20...	136 Brain	163 Backplane	RX, TX to cards and DSP	22
J34	10...	136 Brain	Jog Wheel	Connections to Jog wheel	23
J35	34...	136 Brain	122 Output cntrl surface	Clock, data, ramp (control)	24
J36	26...	136 Brain	121 Line ctrl surface	Clock, data, ramp	25
J37	26...	136 Brain	120 Mic/Line ctrl surface	Clock, data, ramp	26
J38	34...	136 Brain	124 8-way Fader	Clock, data, ramp	27
J39	34...	136 Brain	124 8-way Fader	Clock, data, ramp	27
J40	34...	136 Brain	125 9-way Fader	Clock, data, ramp	28
J41	7	136 Brain	111 Power distribution	Power supply for Brain board	29
J42	4	111 Power distribution	114 DSP	Power supply for DSP board	30
J43	26...	111 Power distribution	110 Analog I/O	Power supply for Analog I/O	30
J47	34...	163 Backplane	114 DSP	Tape I/O data	31
J48	34...	163 Backplane	114 DSP	Tape I/O and Alt I/O data	31
J50	6	163 Backplane	111 Power distribution	Power supply for Backplane	32
J51	62...	163 Backplane	Effects card 1	Effects card slot 1	33
J60	3	121 Line ctrl surface	112 DCA	Talkback circuit	34

J1

This is J1 on the power distribution board, not to be confused with the other J1 shown opposite, or J1 on the DSP board.

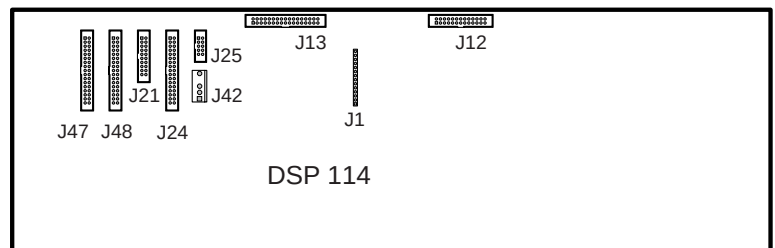
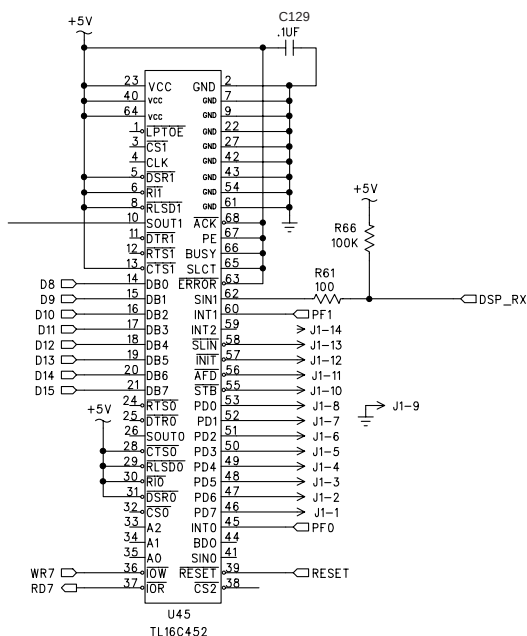


J1 and J2



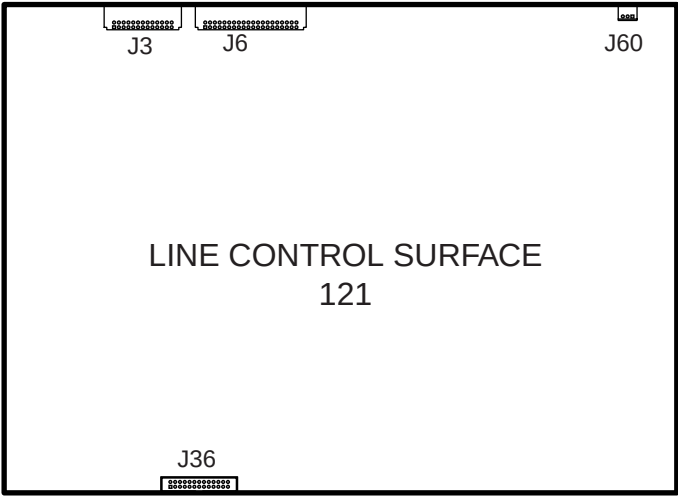
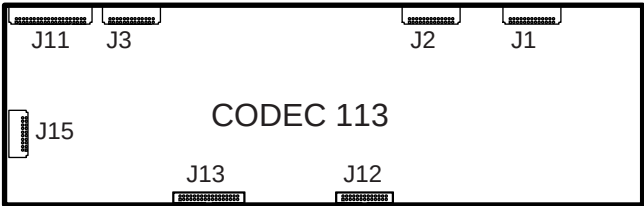
The labels for each pin are the same on the Codec board as on the Analog I/O board. In the following pages, where there are slight differences in labeling, both ends of a connector are shown.

J1 on DSP (unused)

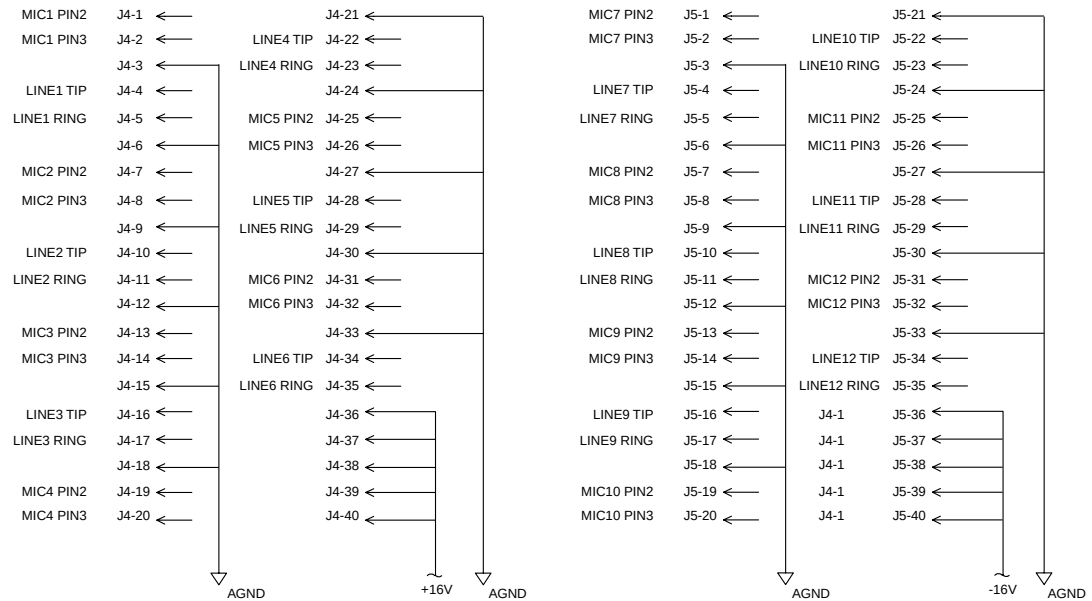


J3

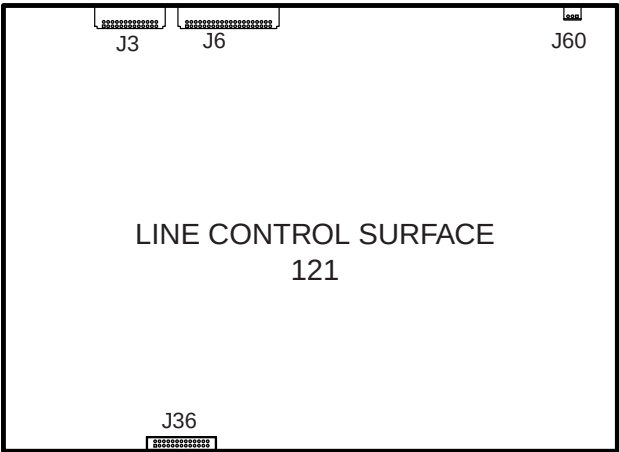
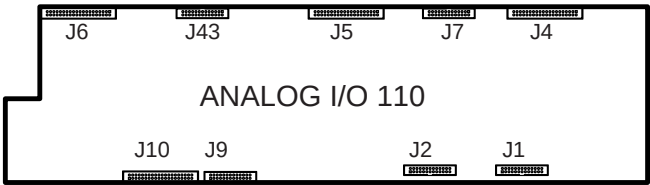
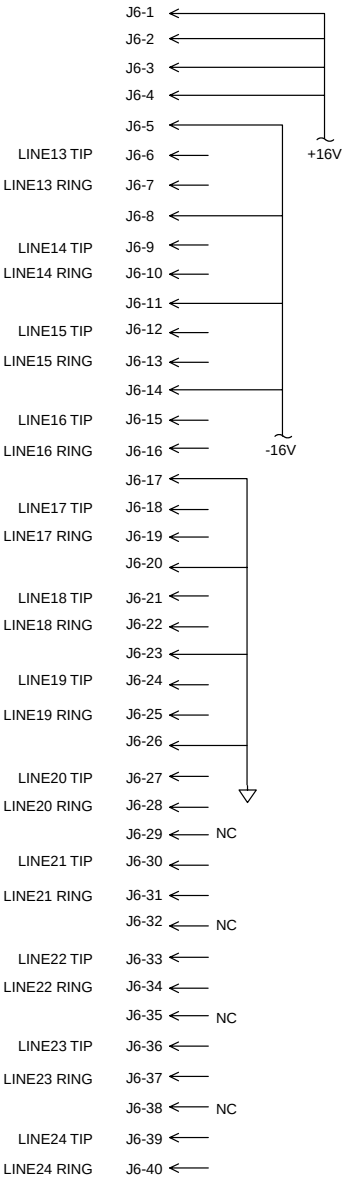
- J3-1 ← □ LINE-13
- J3-2 ← □ LINE-13-GND
- J3-3 ← □ LINE-14
- J3-4 ← □ LINE-14-GND
- J3-5 ← □ LINE-15
- J3-6 ← □ LINE-15-GND
- J3-7 ← □ LINE-16
- J3-8 ← □ LINE-16-GND
- J3-9 ← □ LINE-17
- J3-10 ← □ LINE-17-GND
- J3-11 ← □ LINE-18
- J3-12 ← □ LINE-18-GND
- J3-13 ← □ LINE-19
- J3-14 ← □ LINE-19-GND
- J3-15 ← □ LINE-20
- J3-16 ← □ LINE-20-GND
- J3-17 ← □ LINE-21
- J3-18 ← □ LINE-21-GND
- J3-19 ← □ LINE-22
- J3-20 ← □ LINE-22-GND
- J3-21 ← □ LINE-23
- J3-22 ← □ LINE-23-GND
- J3-23 ← □ LINE-24
- J3-24 ← □ LINE-24-GND
- J3-25 ←
- J3-26 ←



J4, J5 and J7



J6



J8

NOTE although the busses are labeled like this on all schematics, they are swapped in software, so Bus1 on schematic = Bus 8 really

Bus 2 = Bus 7 really

Bus 3 = Bus 6

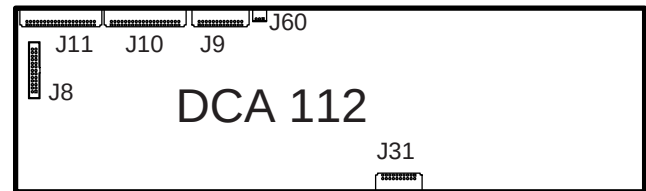
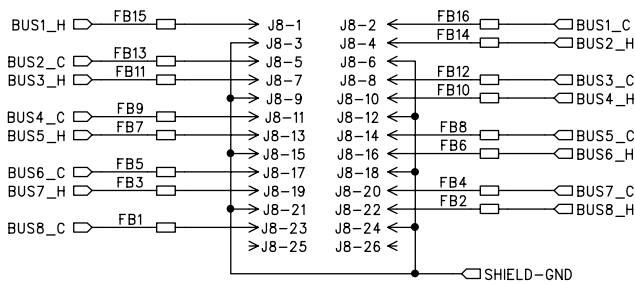
Bus 4 = Bus 5

Bus 5 = Bus 4

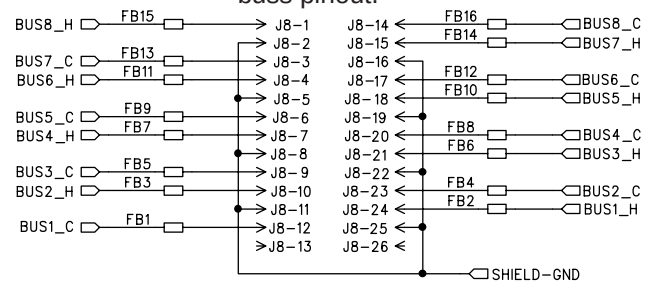
Bus 6 = Bus 3

Bus 7 = Bus 2

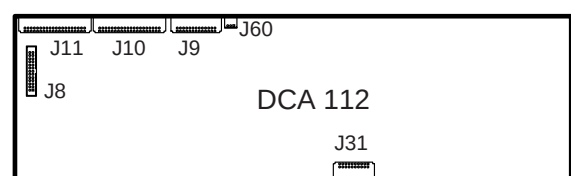
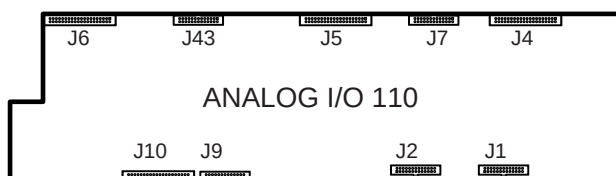
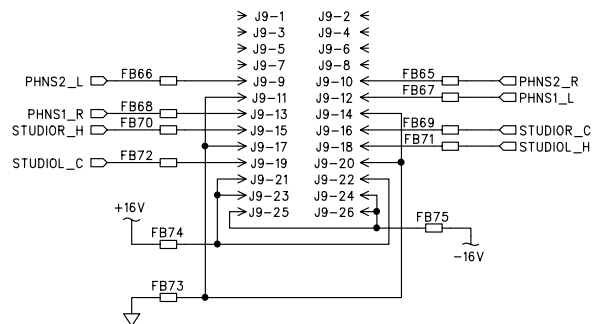
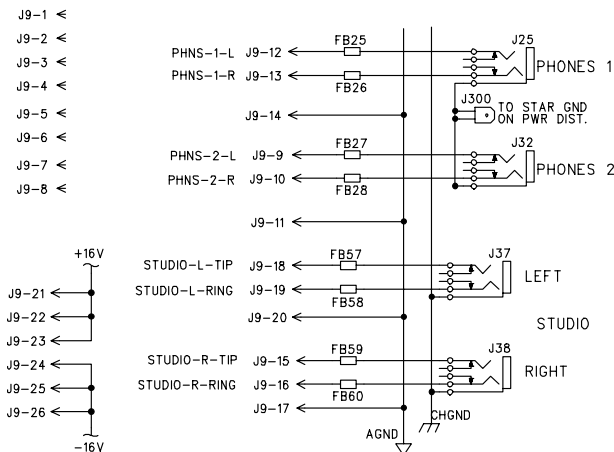
Bus 8 = Bus 1



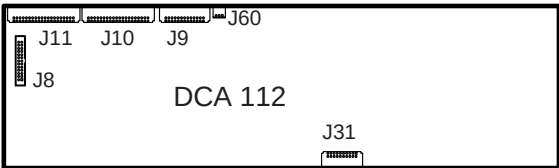
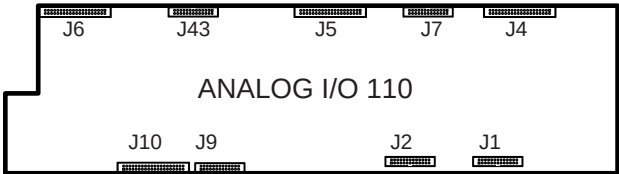
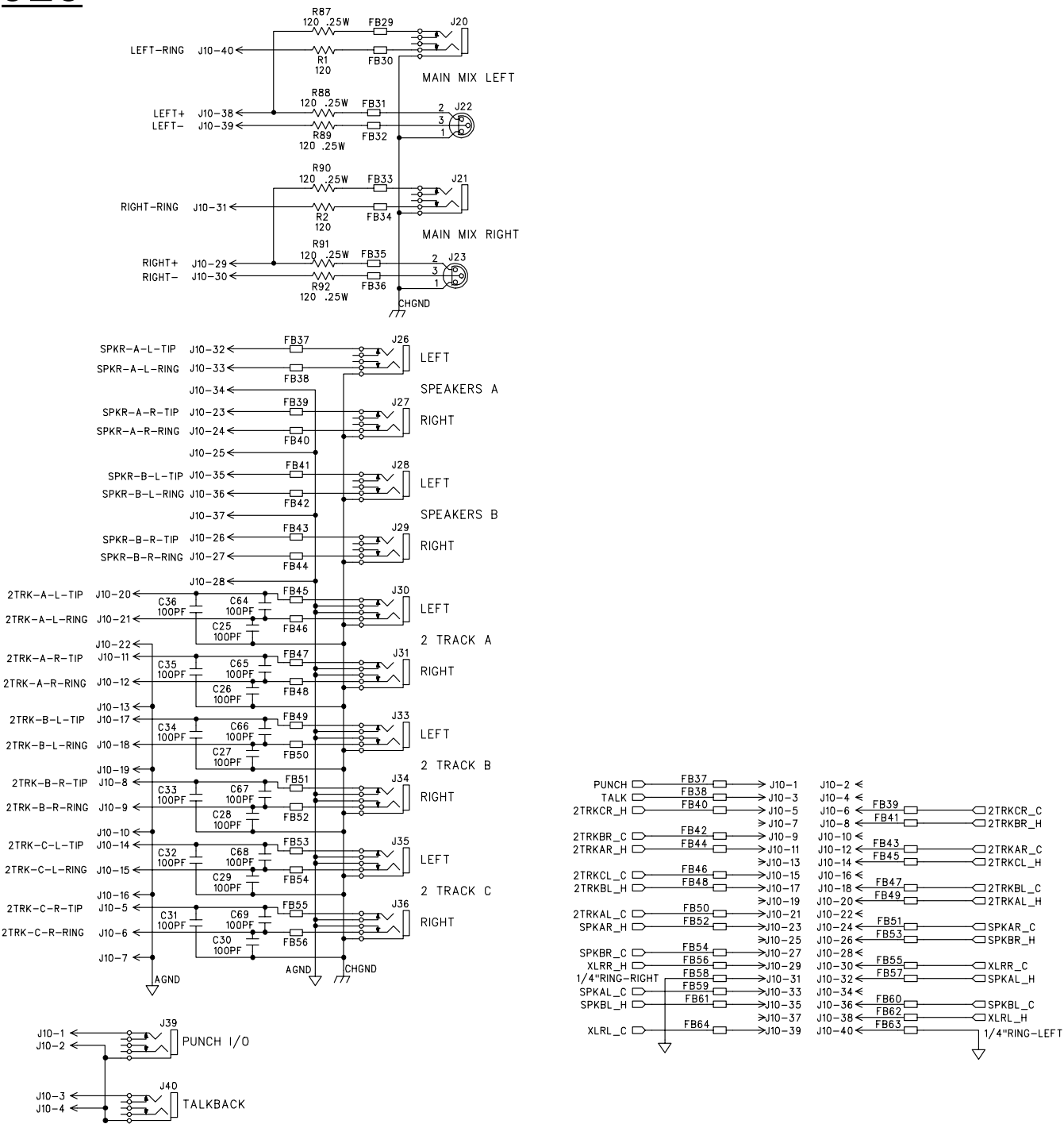
DB25 connector
to outside world
NOTE this is the correct
buss pinout.



J9



J10



J11

NOTE although the busses are labeled like this on all these schematics, they are swapped in software, so Bus1 on schematic = Bus 8 really

Bus 2 = Bus 7 really

Bus 3 = Bus 6

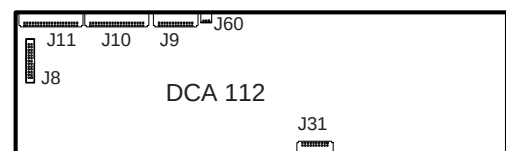
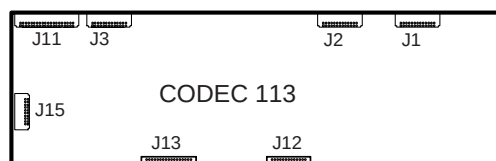
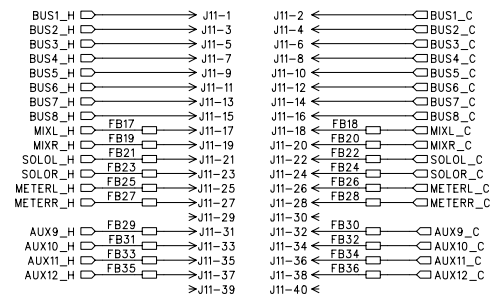
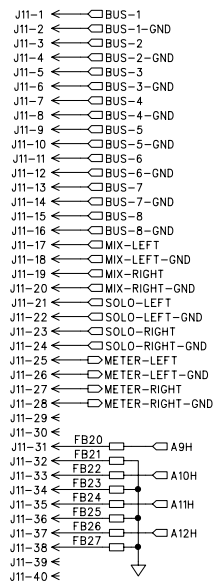
Bus 4 = Bus 5

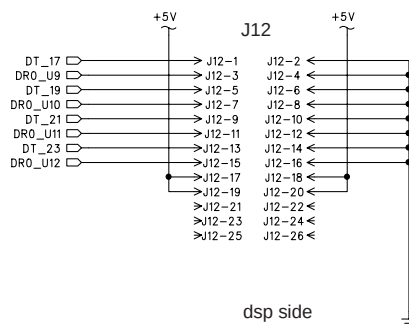
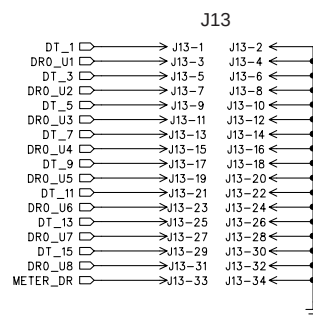
Bus 5 = Bus 4

Bus 6 = Bus 3

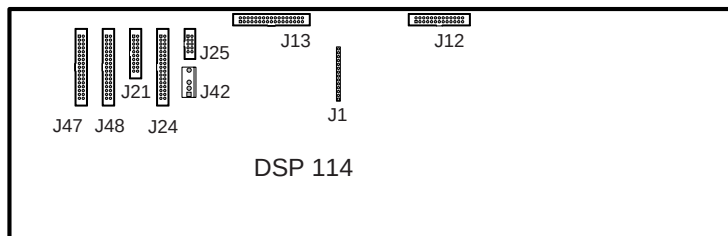
Bus 7 = Bus 2

Bus 8 = Bus 1

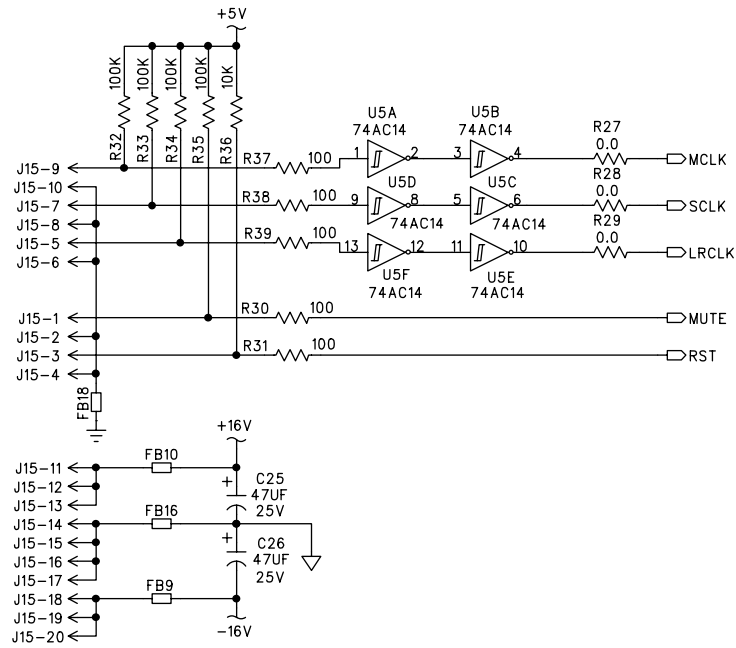




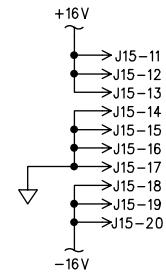
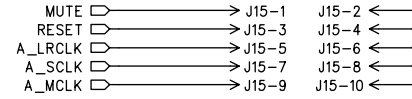
dsp side



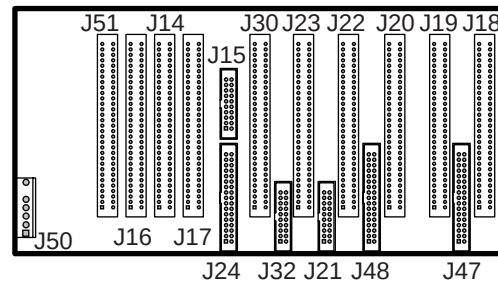
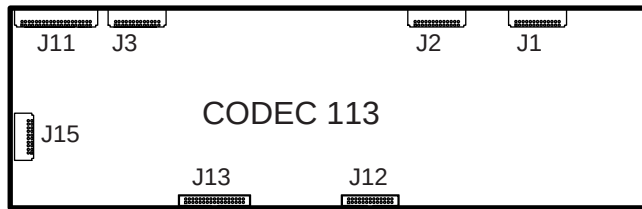
J15



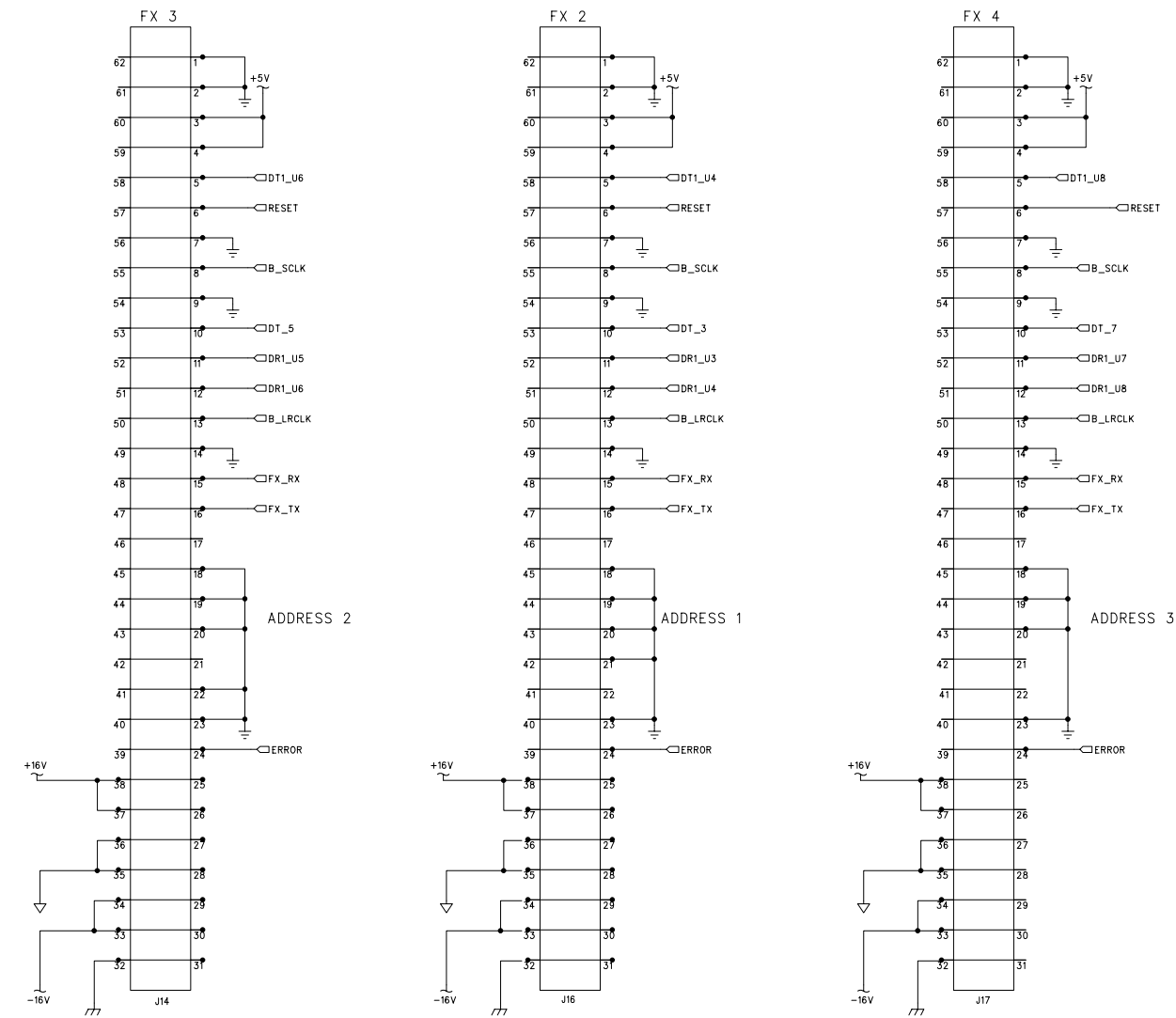
TO CODEC



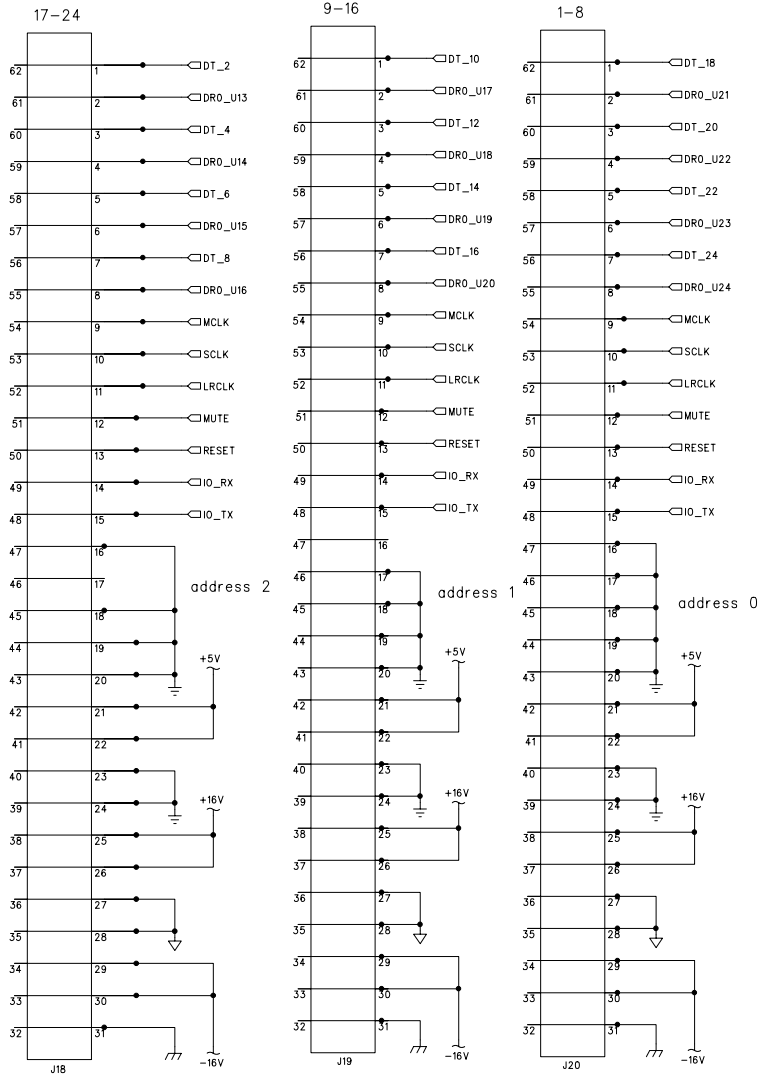
BACKPLANE 163



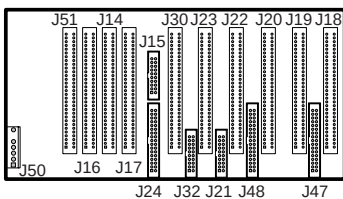
J14, J16 and J17 (on Backplane)



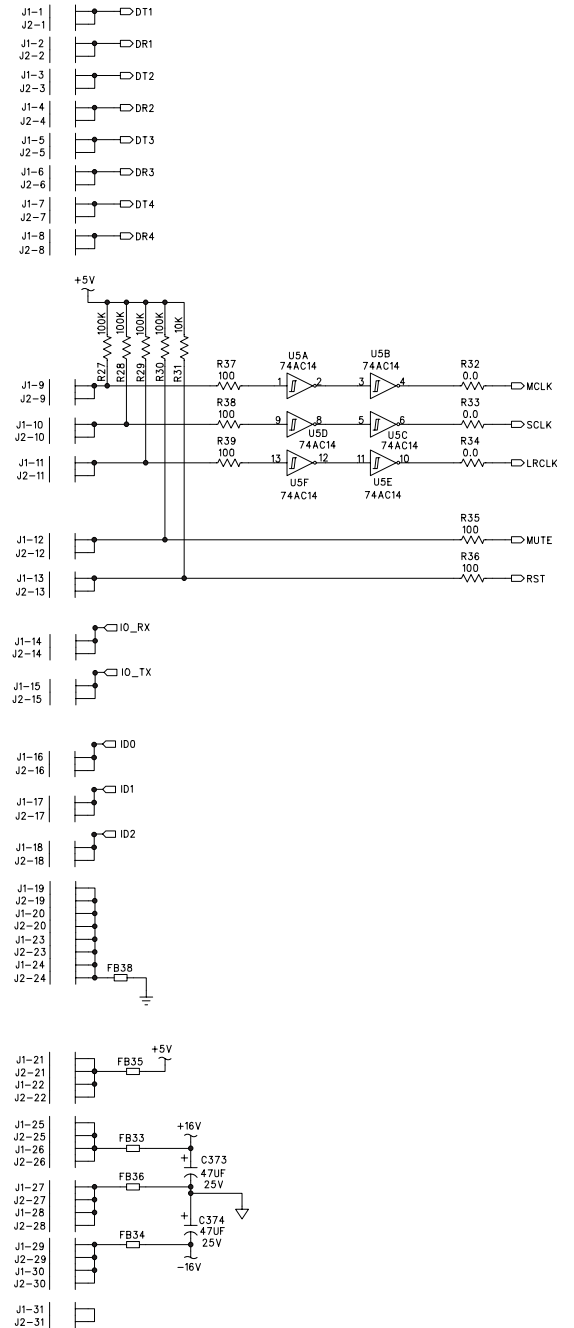
J18, J19, J20



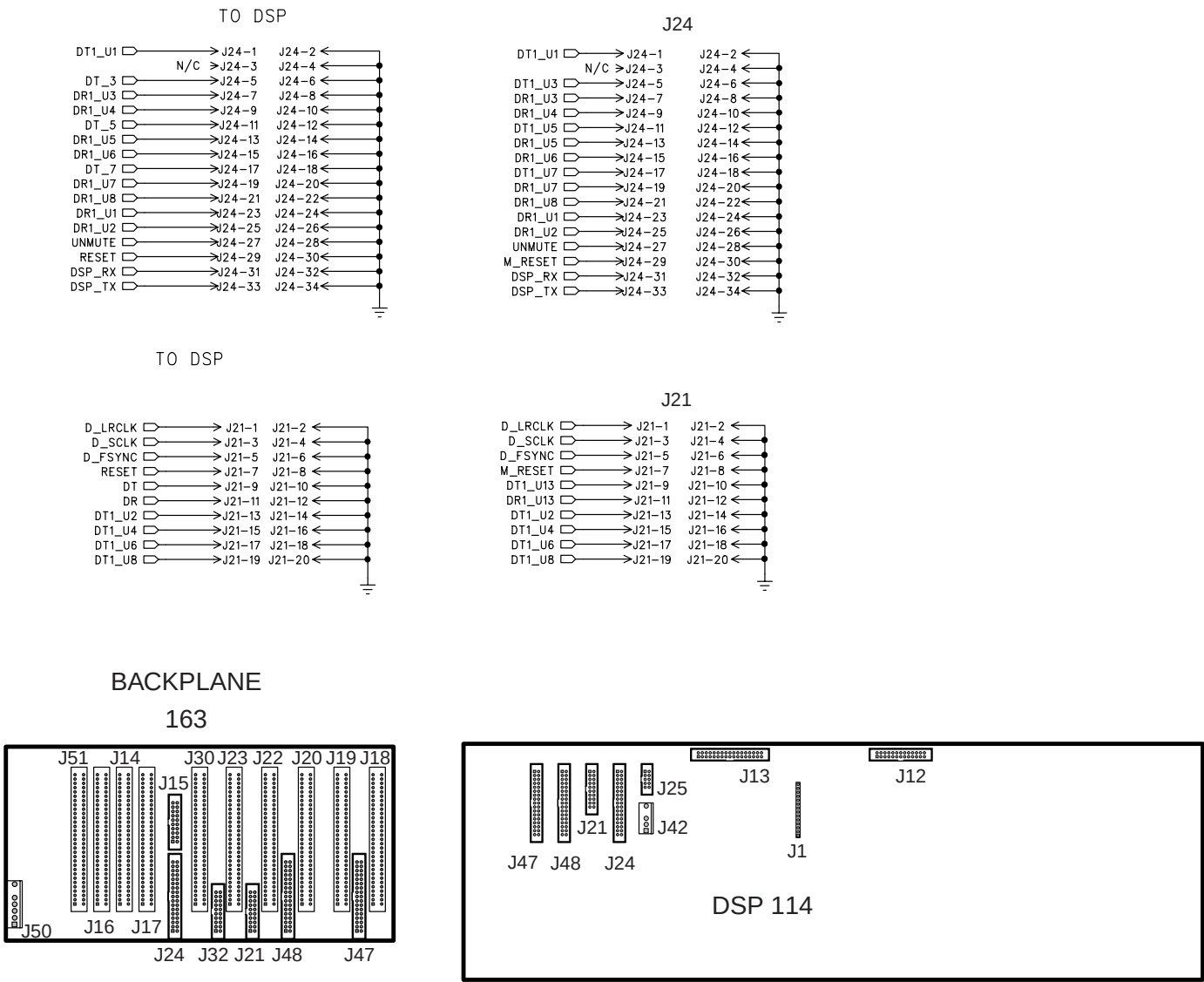
BACKPLANE 163



TAPE I/O CARD 119



J21 and J24



EXTRA

62 1 DT1_U9

61 2 DR1_U9

60 3 DT1_U10

59 4 DR1_U10

58 5 DT1_U11

57 6 DR1_U11

56 7 DT1_U12

55 8 DR1_U12

54 9 MCLK

53 10 SCLK

52 11 LRLCLK

51 12 MUTE

50 13 RESET

49 14 IO_RX

48 15 IO_TX

47 16

46 17

45 18 address 3

44 19

43 20 +5V

42 21

41 22

40 23

39 24 +16V

38 25

37 26

36 27

35 28

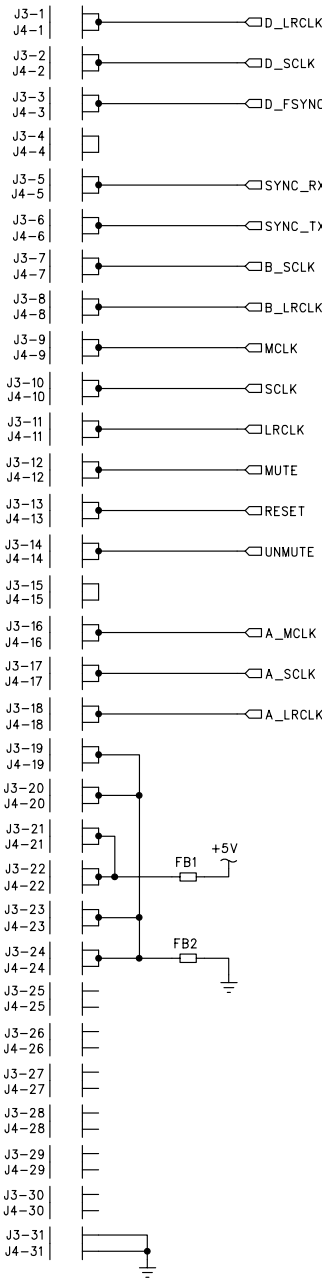
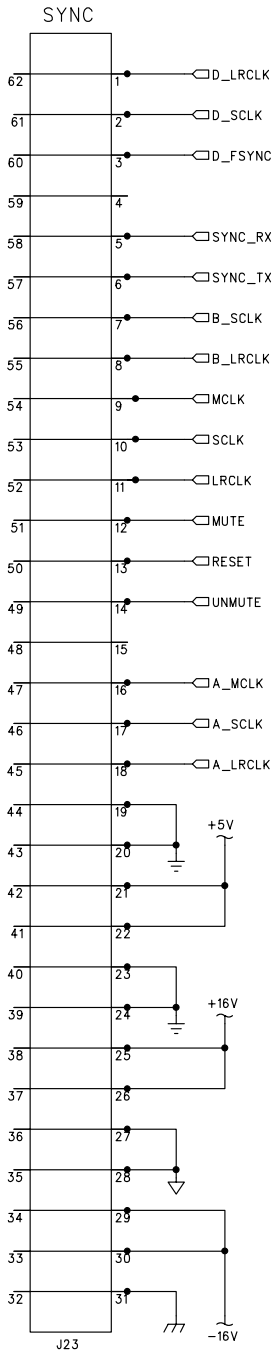
34 29

33 30

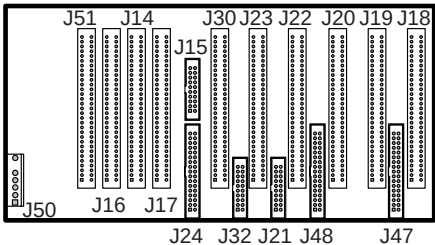
32 31 -16V

J22

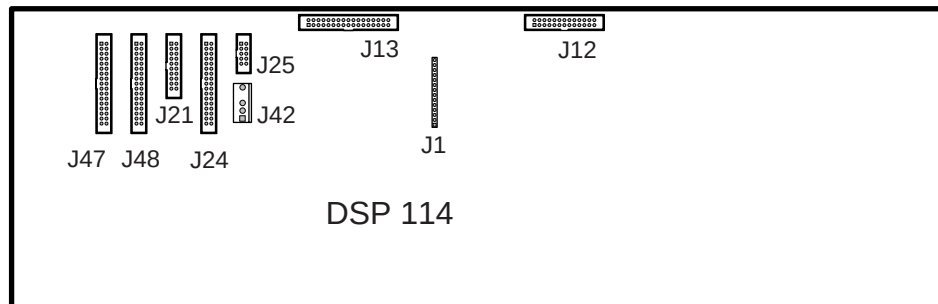
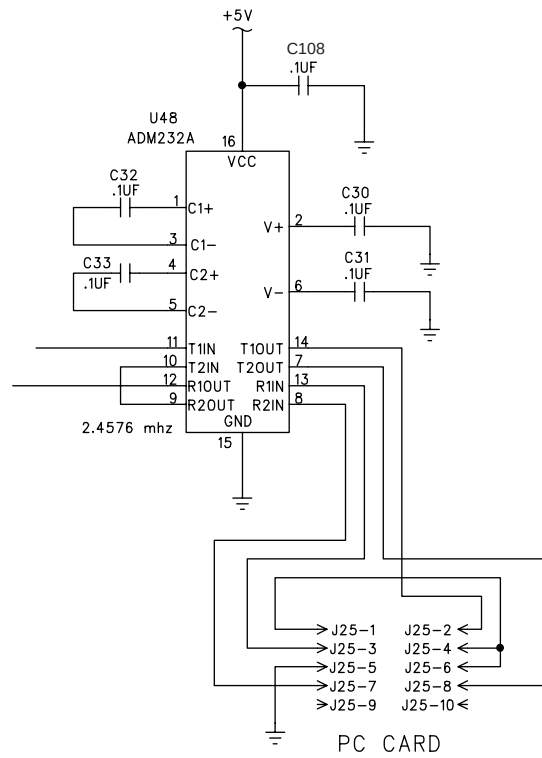
J23



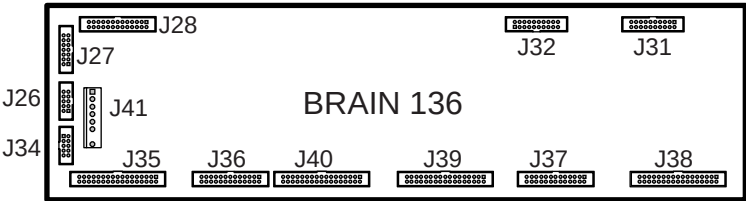
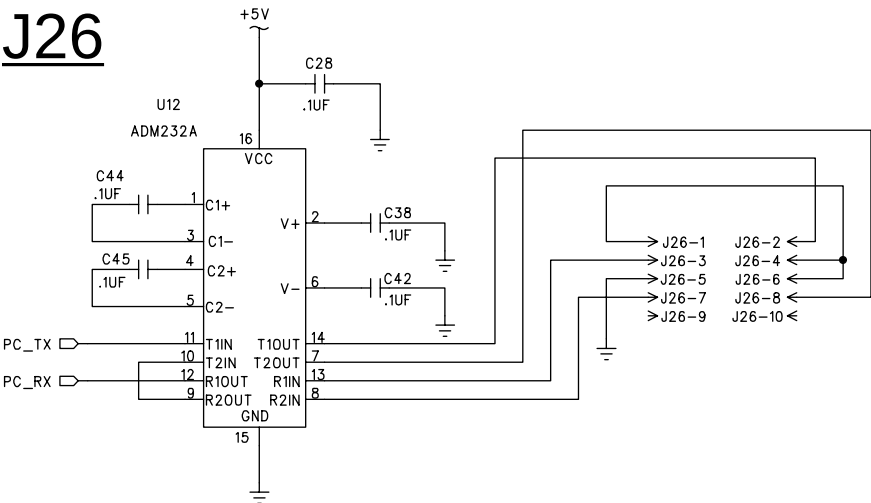
BACKPLANE
163



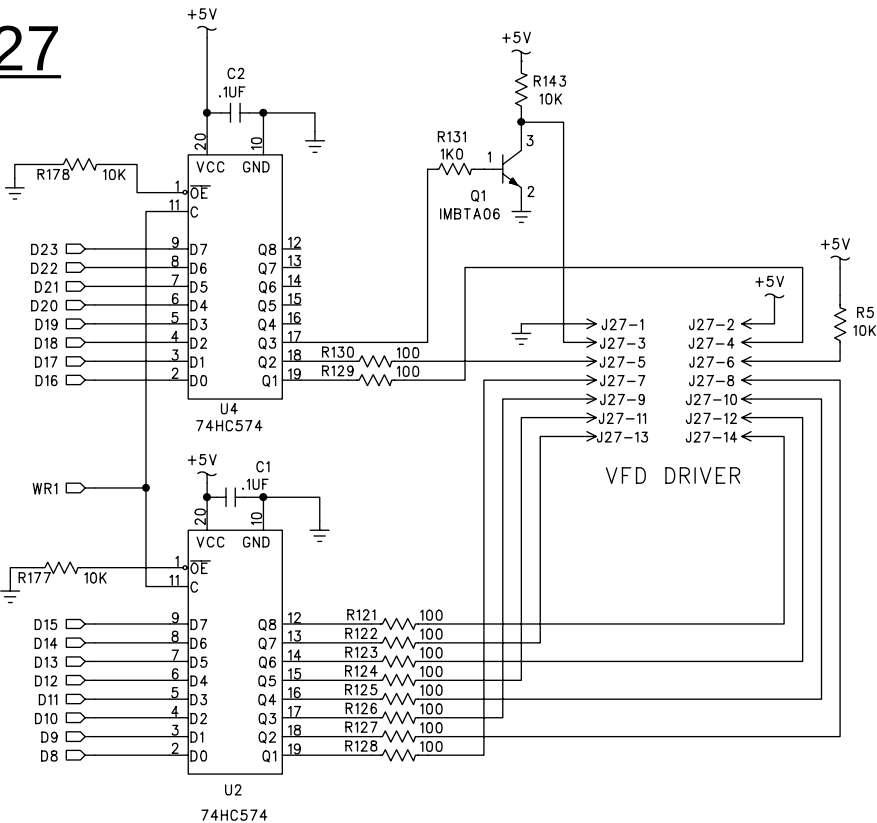
CLOCK CARD
164

J25

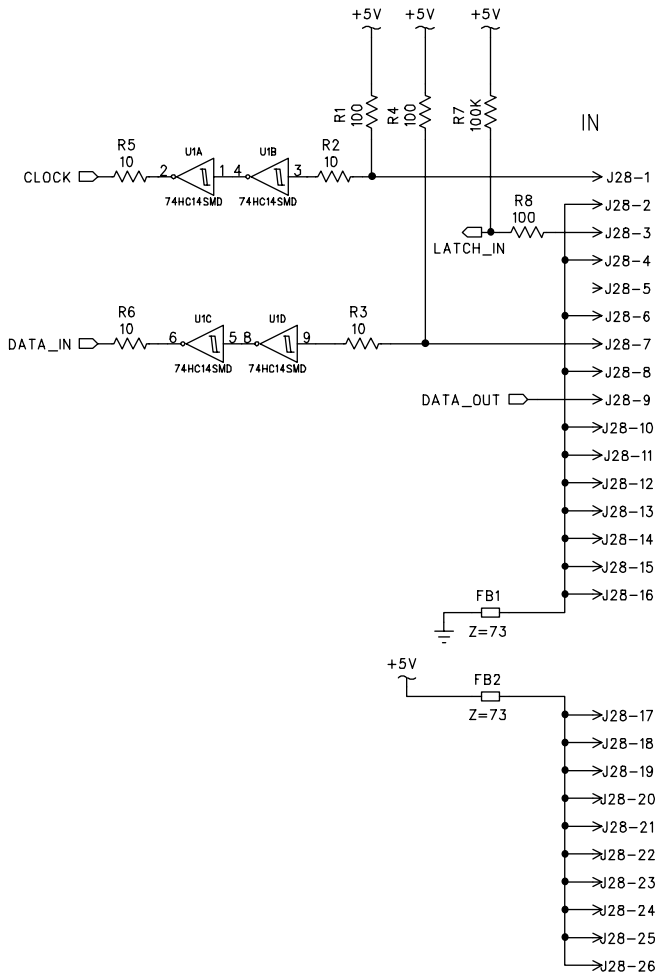
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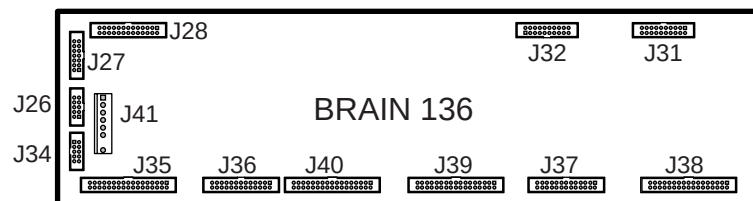
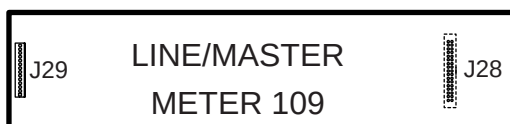
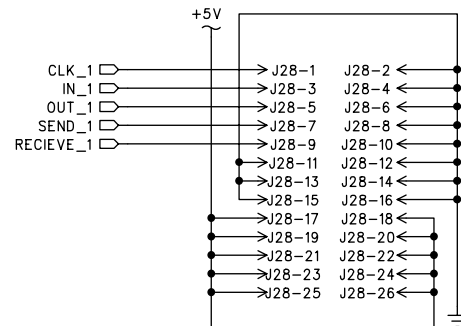
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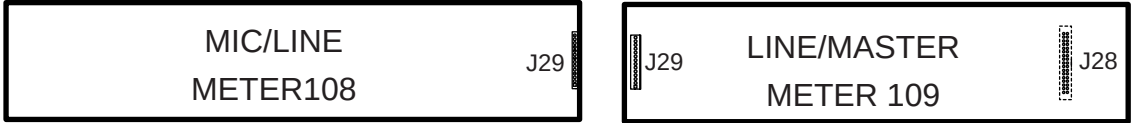
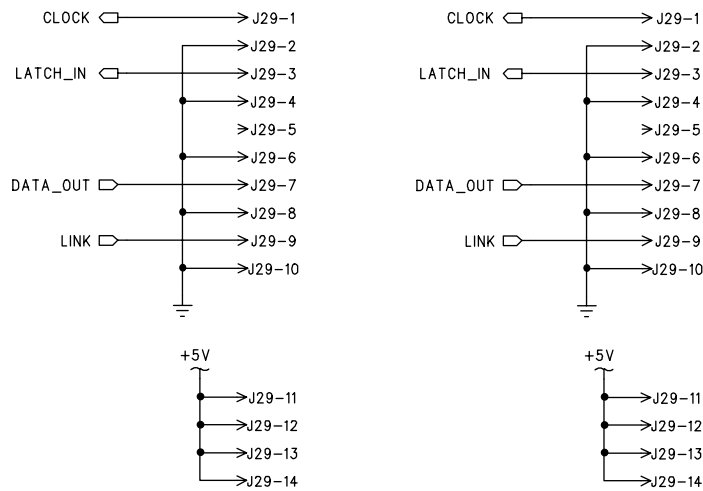
J28

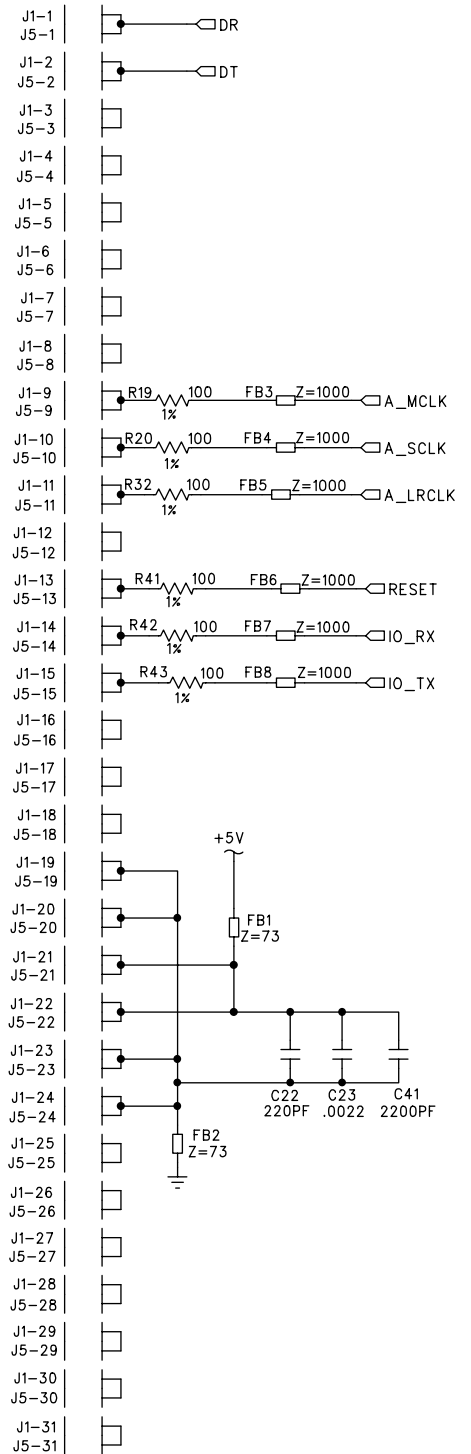
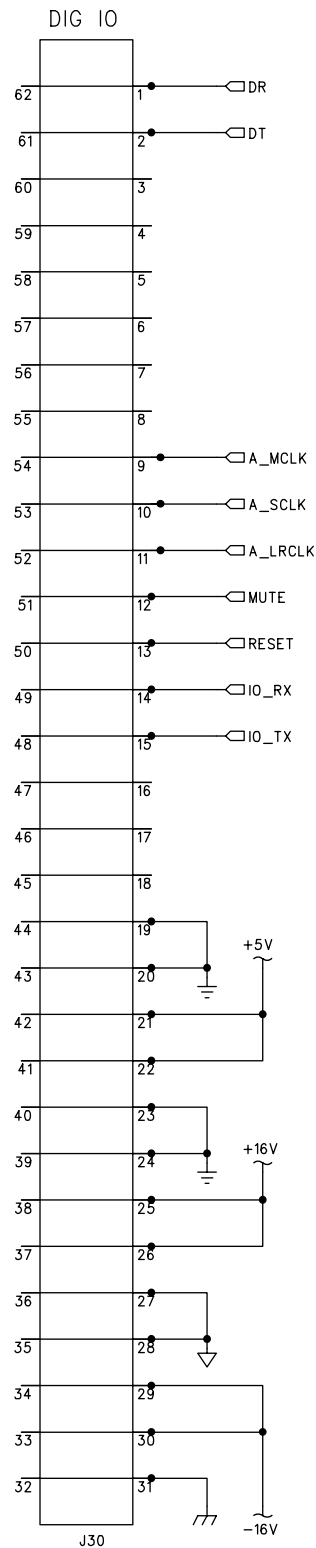
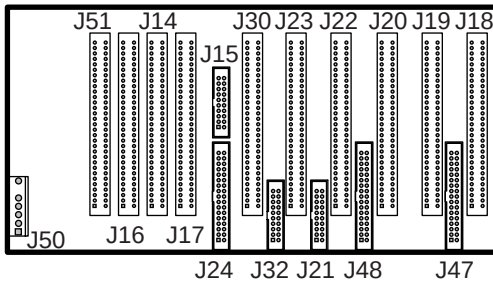


METER CARD (to U5-7,11,31 page 3)

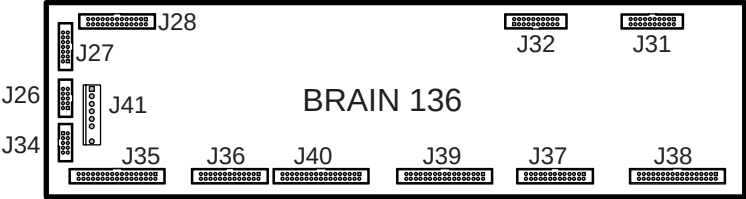
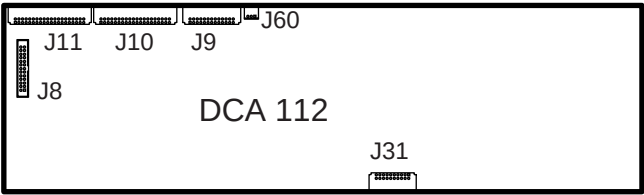
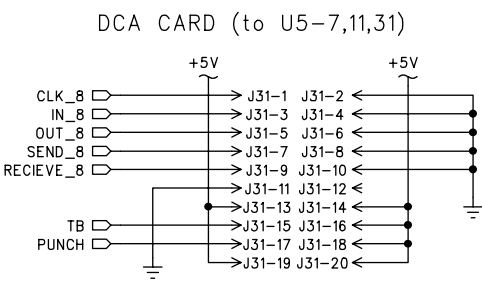
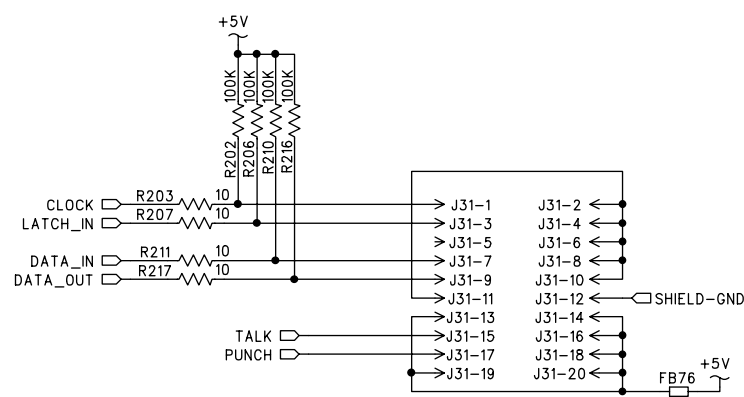


J29

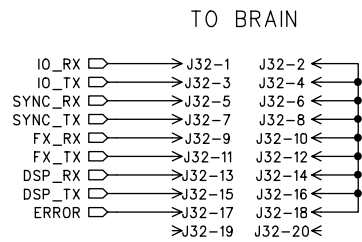
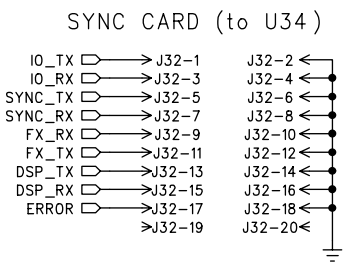


J30**BACKPLANE 163****DIGITAL I/O CARD 115**

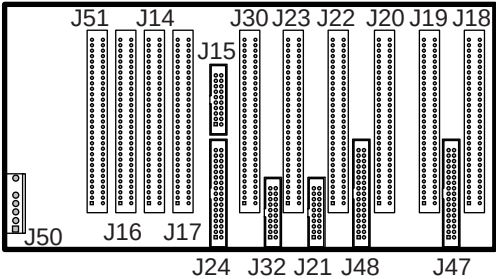
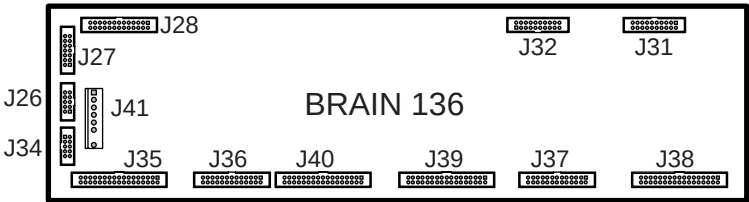
J31



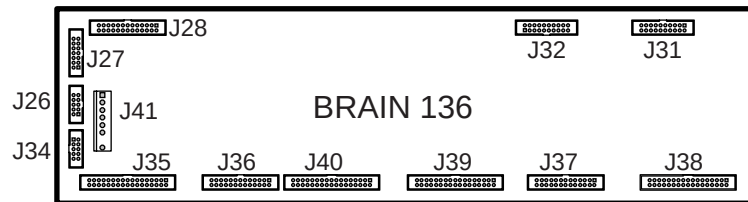
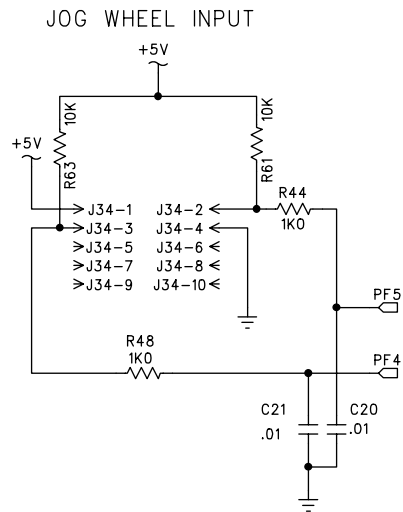
J32



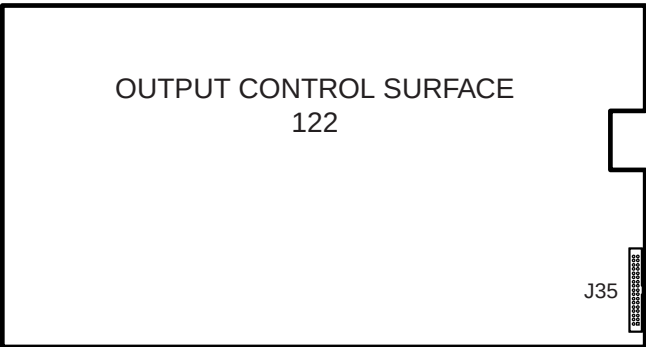
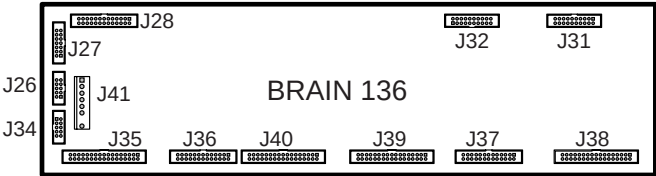
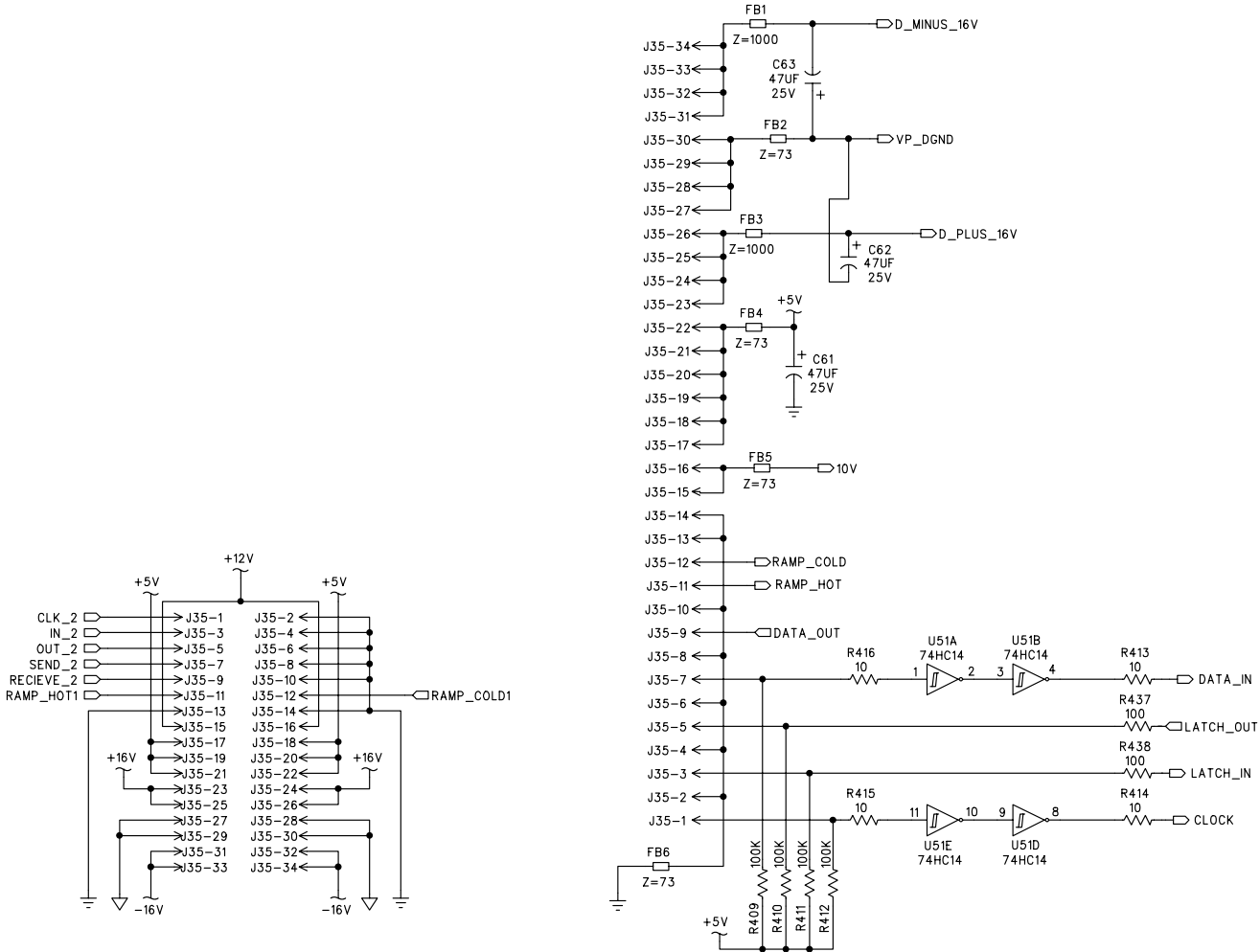
BACKPLANE
163



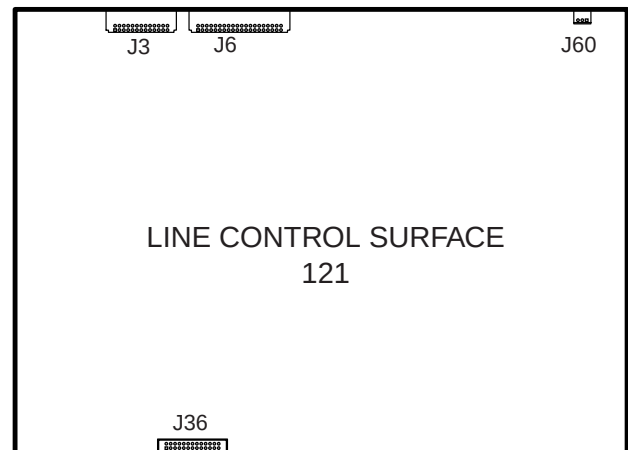
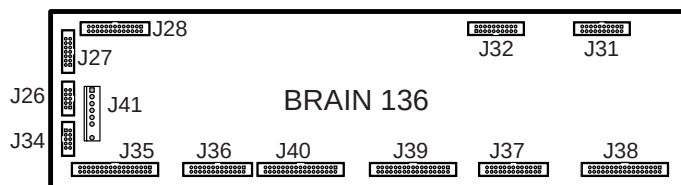
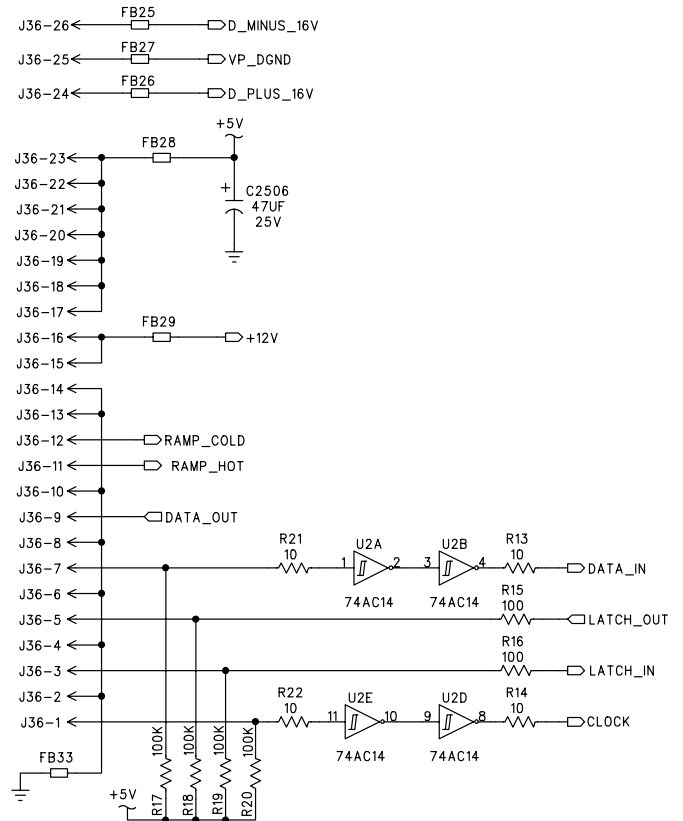
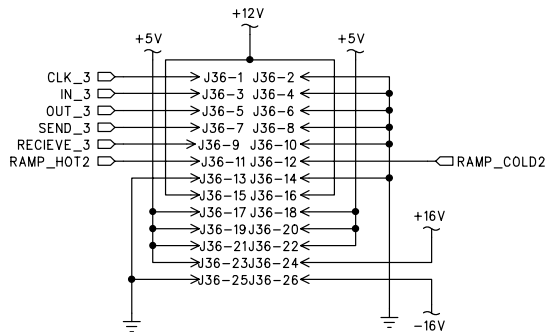
J34



J35

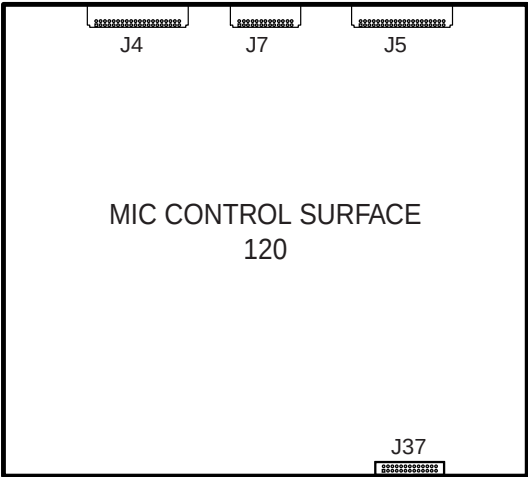
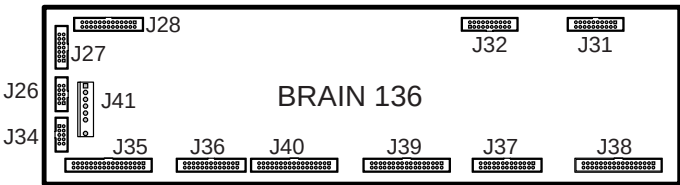
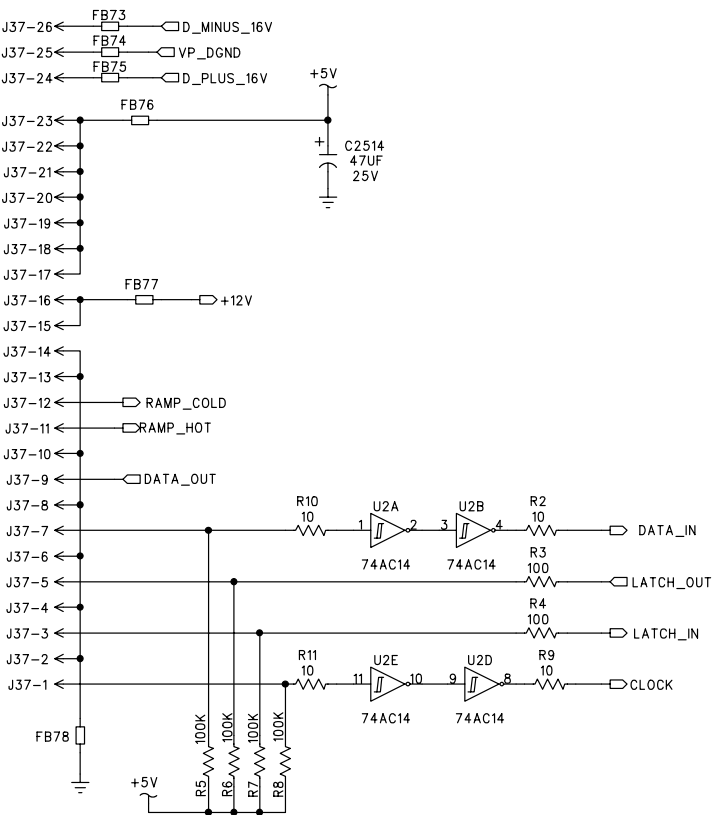
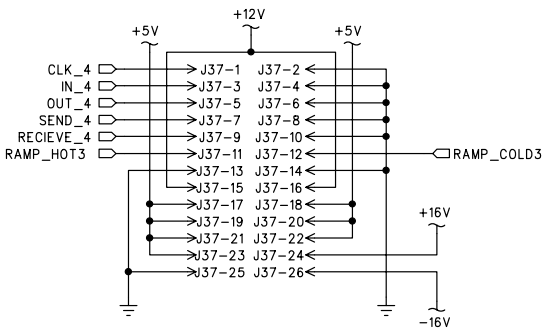


J36

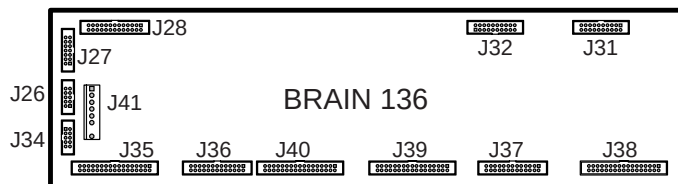
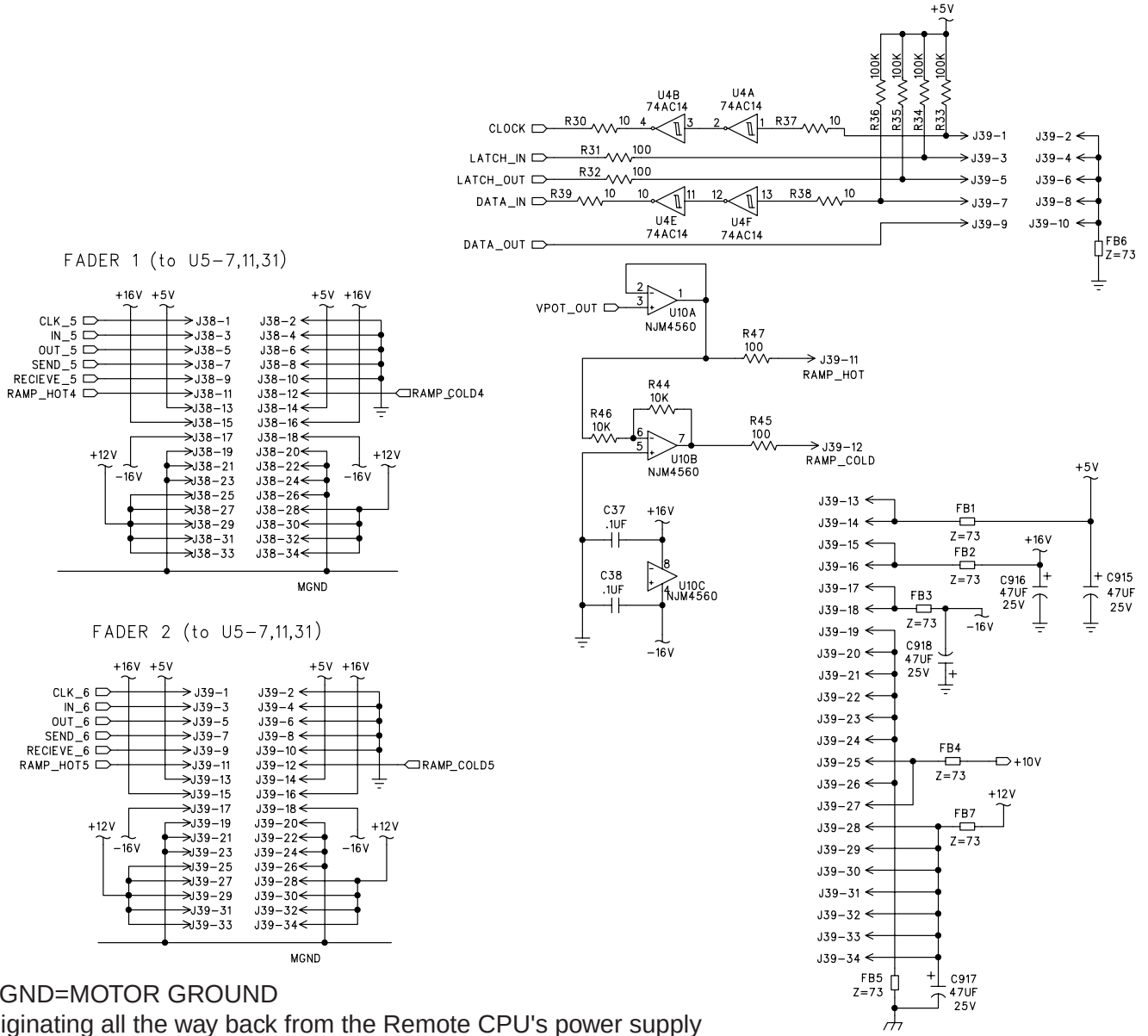


J37

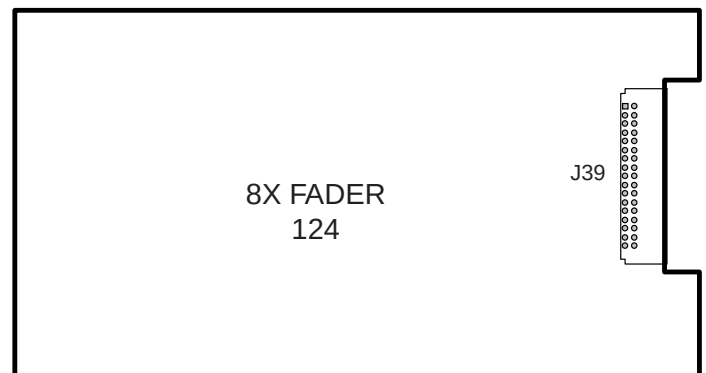
MIC CARD (to U5-7,11,31)



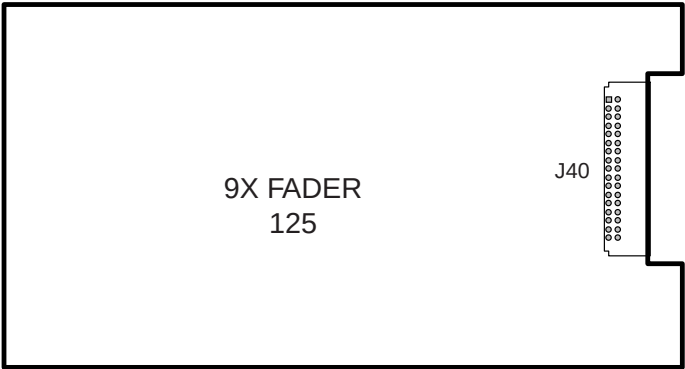
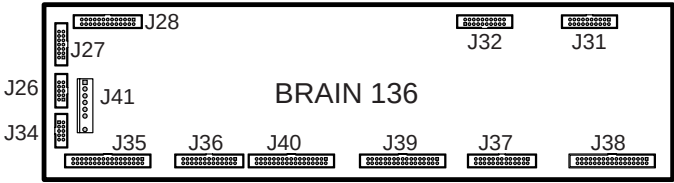
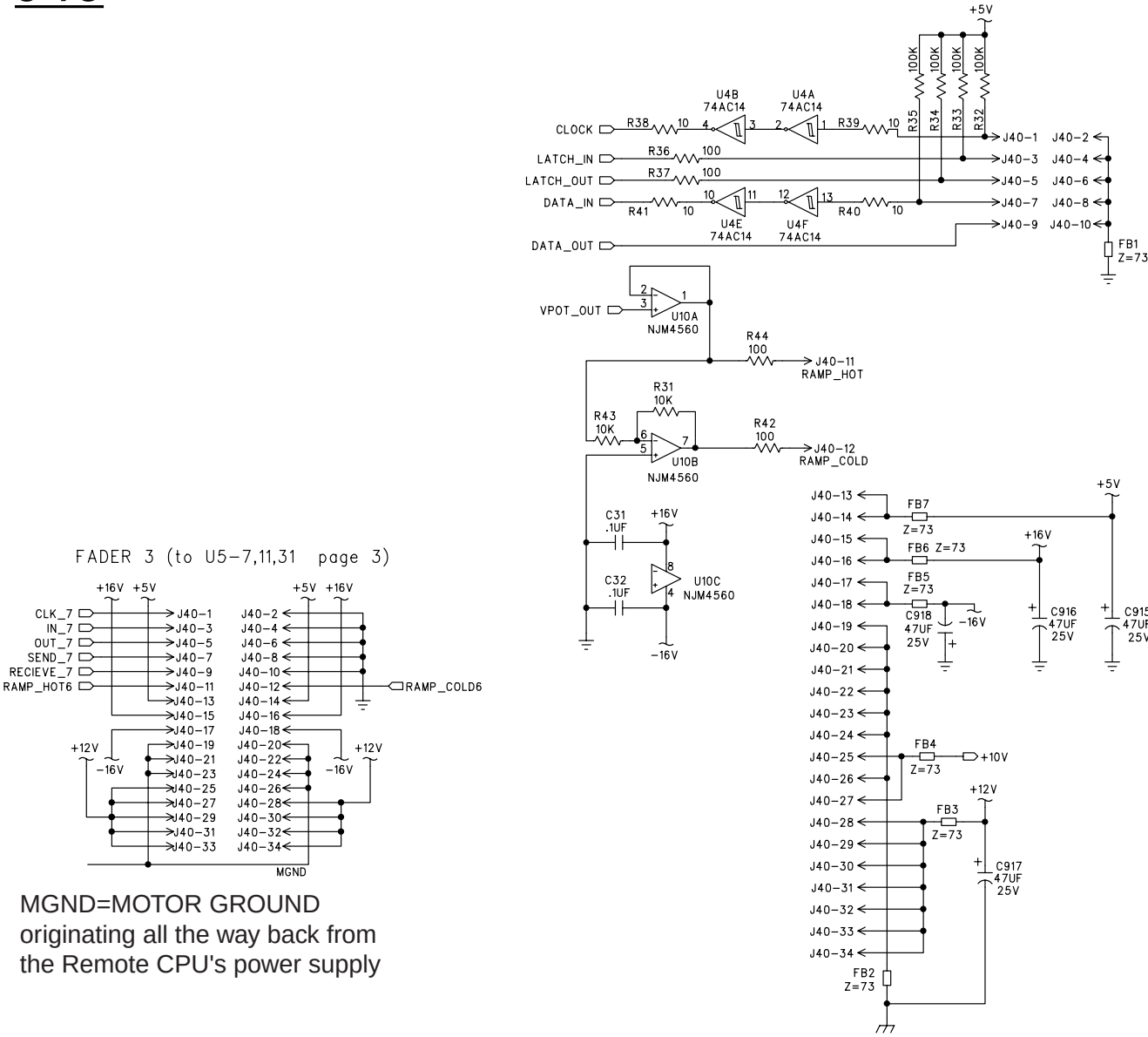
J38 and J39

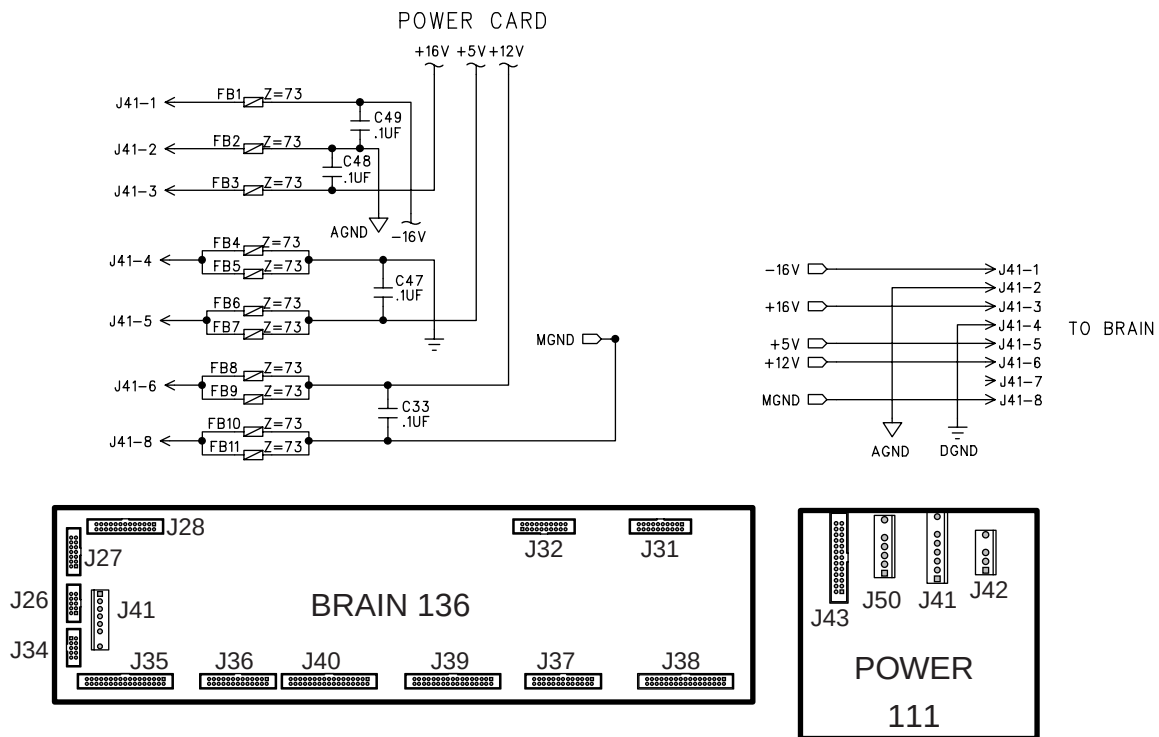


Note that J38 on the Brain board, connects to J39 on Fader 1.
J39 on the Brain board connects to J39 on Fader 2.

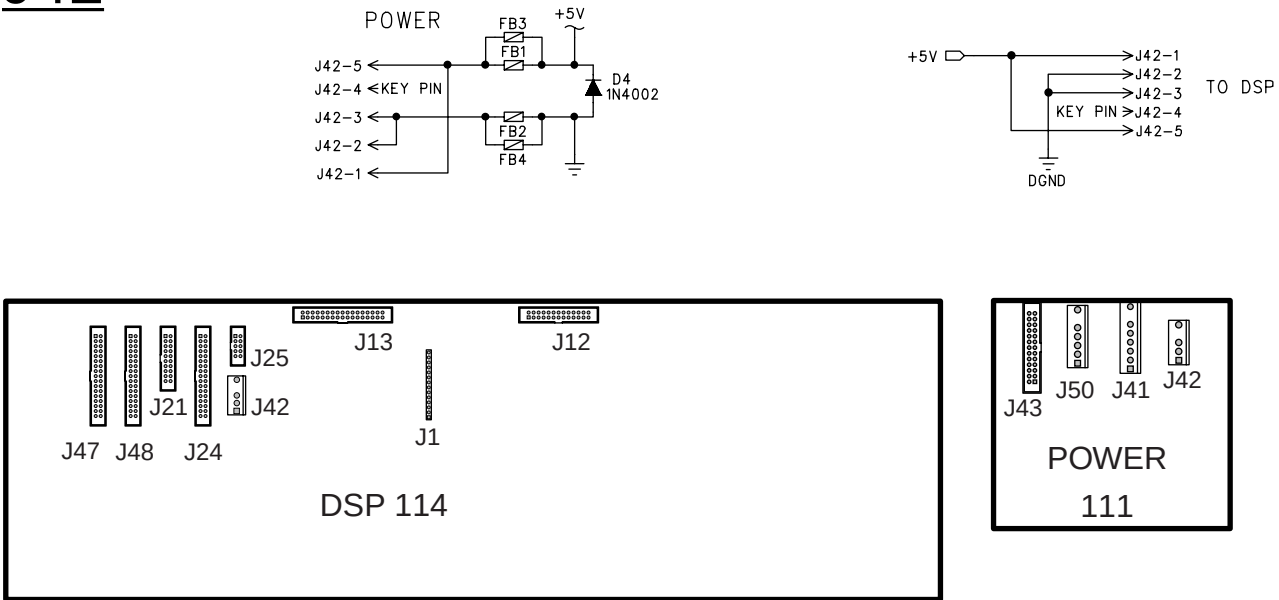


J40

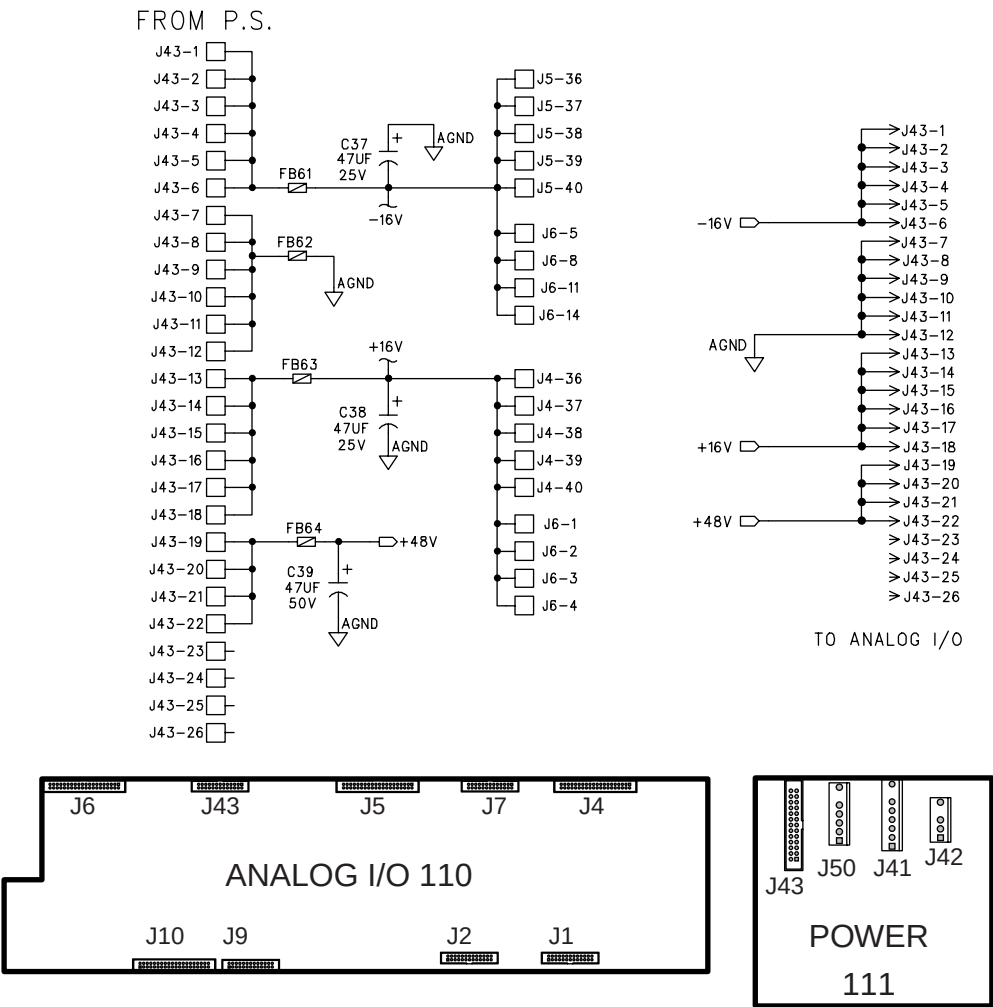


J41

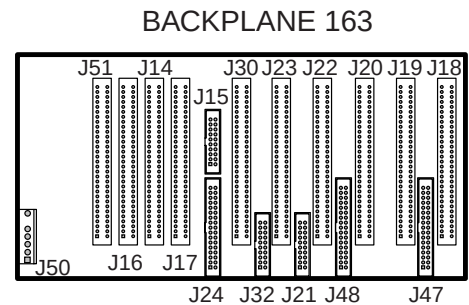
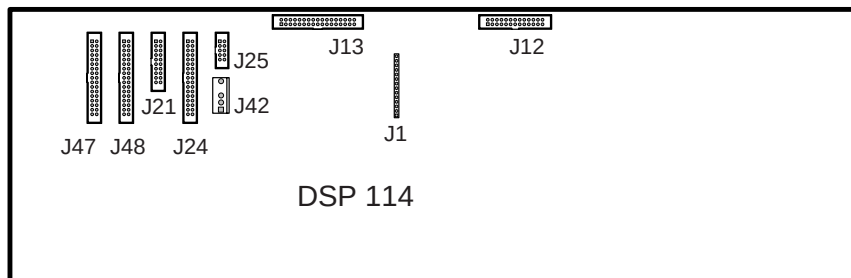
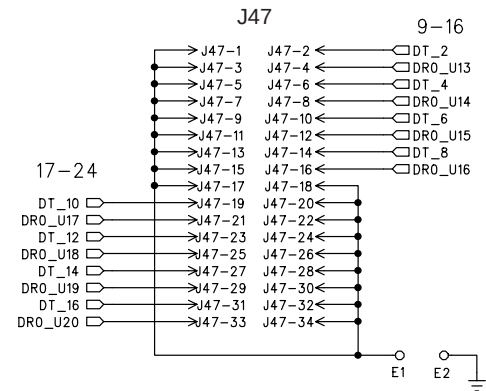
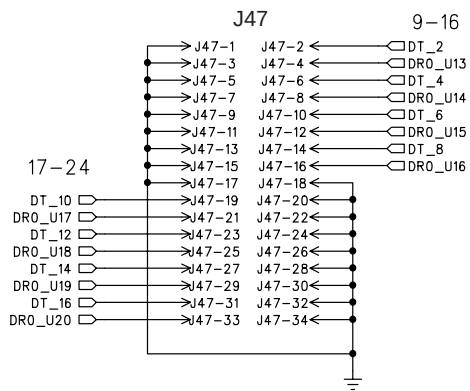
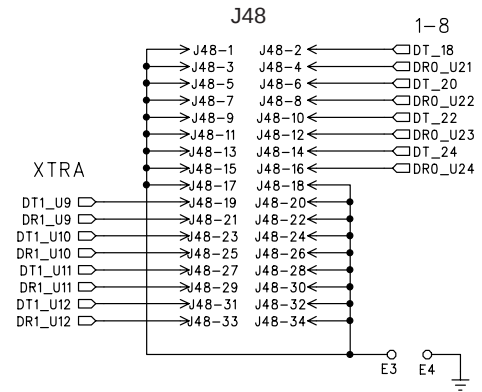
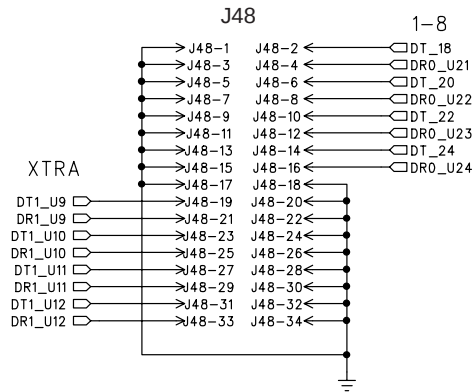
J42



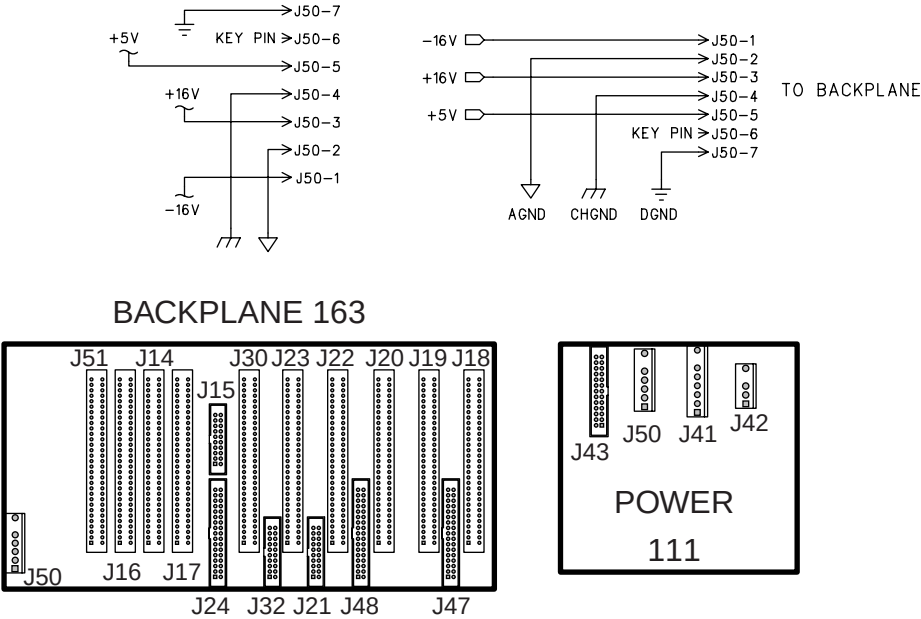
J43

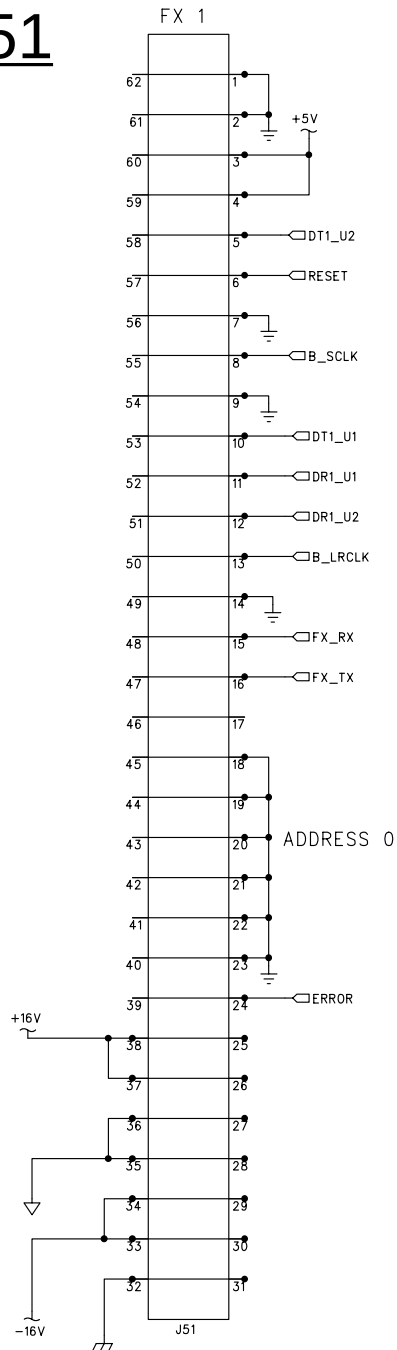


J47 and J48



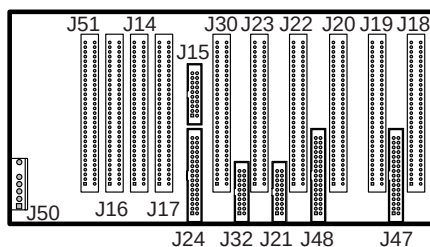
J50



J51

BACKPLANE

163



J60

