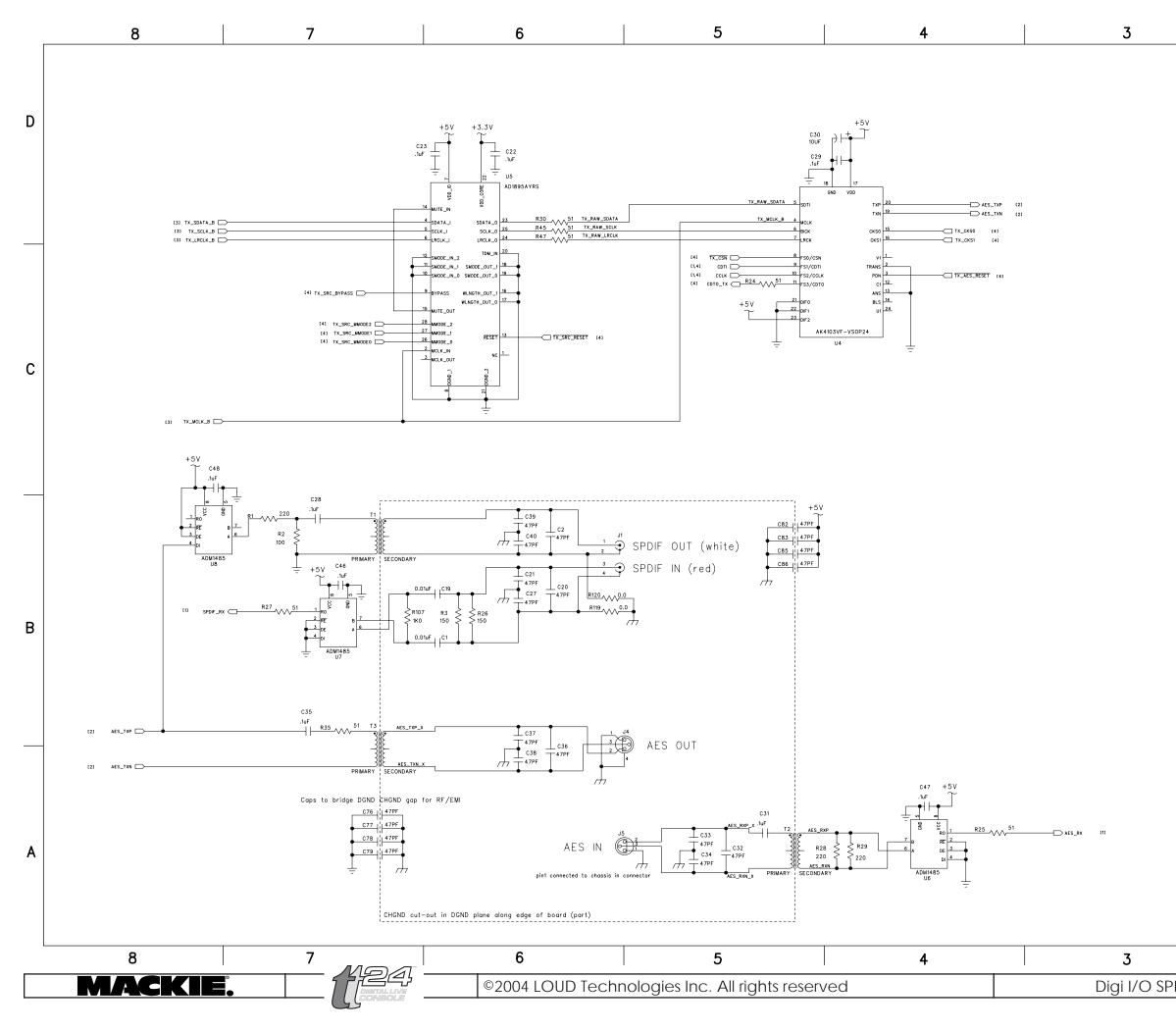


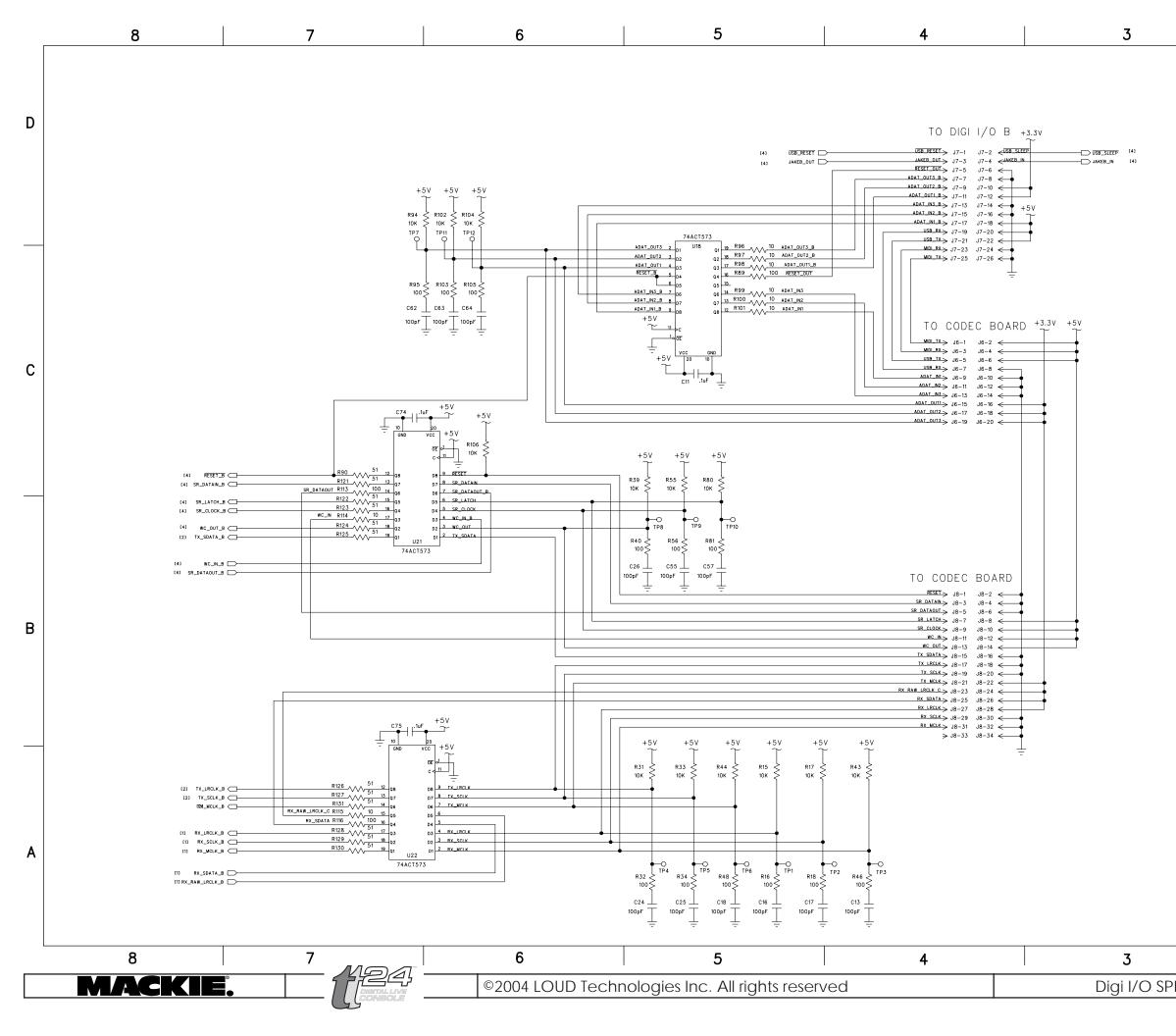
DWG. NO. 0006879-00 SHT REV: A00 1	
ECO# REV: DESCRIPTION REV. BY: APP. BY: D	D
51RX_SDATA_B (3) RX_SCLK_B (3) RX_LRCLK_B (3)	С
X_SRC_RESET (4)	В
APPROVALS DATE DRAWN: GCJ 10/05/03	
CHECKED: Mackie Designs Inc. Woodinville, Wa.   NP ENG: DMR Digi IO SPDIF - SPDIF/AES/W   MFG: SIZE SCALE: DWG. NO.	rev. A00

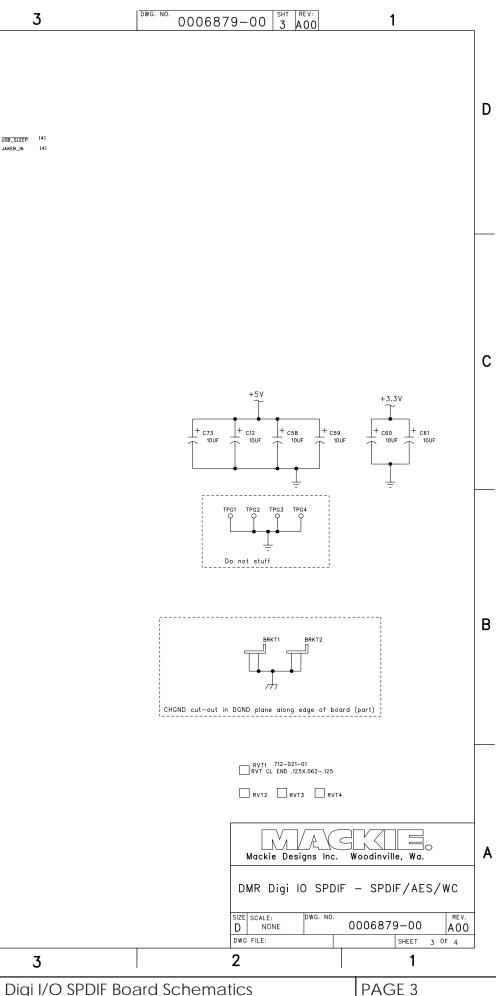


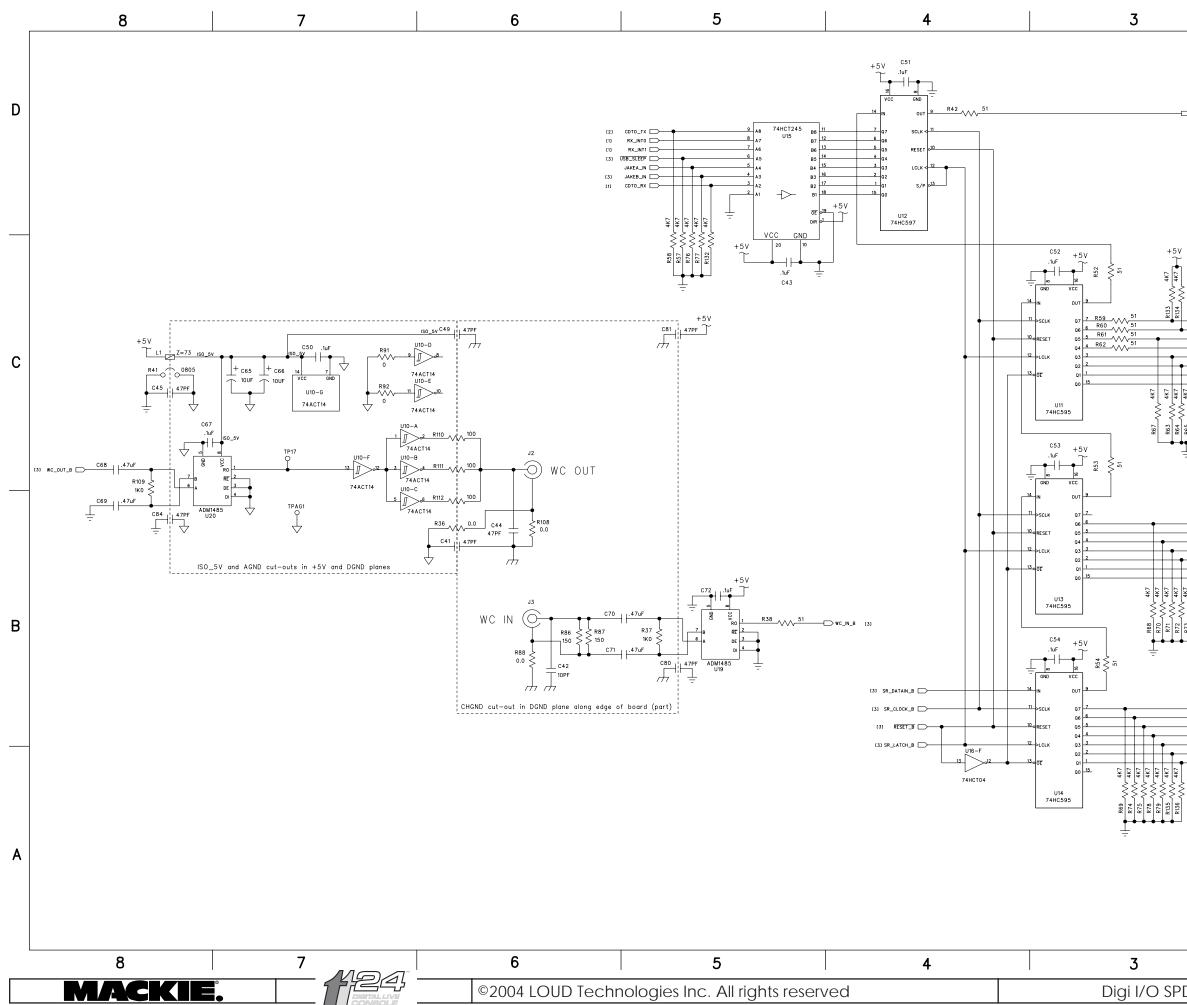
E	B
DMR Digi IO SPDIF - SPDIF/AES/WC	A
SIZE SCALE: DWG. NO. 0006879-00 REV.   DWG FILE: SHEET 2 OF 4	
2 1	
DIF Board Schematics PAGE 2	

D

С







3	DWG. NO. 000687	9-00 4 A00	1	_
−−−⊂ SR_DATAOUT_B	(3)			D
	C_CSN [2] CLK [1,2]			С
	C_WOTE (1) (_SRC_BYPASS (1) (_SRC_MMODE1 (1) (_SRC_MMODE1 (1) (_SRC_BYPASS (2) (_SRC_MMODE0 (2) (_SRC_MMODE1 (2)			В
	(_CKSO (2)	SIZE SCALE: DWG. NO. D NONE DWG FILE:	IF – SPDIF/AES/WC 0006879-00 A00 SHEET 4 OF 4	
		2		
Digi I/O SPDIF Boa	ard Schemat	ICS	PAGE 4	