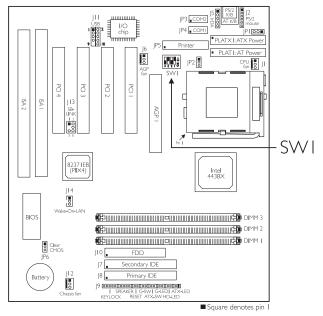
CB50-BX



DIP Switch Settings of the Processors

ſ	Processor		Frequency		Processor		Frequency	0.4/1	
	66MHz	100MHz	Ratio	SWI	66MHz	100MHz	Ratio	SWI	
	300MHz	Future processor	4.5×		433MHz	Future processor	6.5×		
	333MHz	Future processor	5x	ov Nov Nov Nov Nov Nov Nov Nov Nov Nov N	466MHz	Future processor	7x	+ E 7 0Z	
	366MHz	Future processor	5.5×	↓ 20	Future processor	Future processor	7.5×	→ 2 0 2 4	
	400MHz	Future processor	6x	↓ € 7 1 0 X 0	Future processor	Future processor	8×	0Z	

Black rectangle denotes the part that is protruding, the "adjustable" switch



In the example above:

Switch I: Off Switch 2: On

Switch 3: Off

Switch 4: On

Note: I. The voltage regulator will automatically be set according to the voltage of the processor.

You cannot overclock an Intel Celeron[™] processor (PPGA for Socket 370) because its frequency ratio has been fixed by the manufacturer. The table above is for factory use only.

JPI (Wake-On-Keyboard/Mouse) - 2-3 On: Enable; I-2 On: Disable (default) By default, JPI is disabled. Make sure "Keyboard/Mouse Power On" in the Integrated Peripherals setup of the Award BIOS is also disabled. If JPI was previously enabled with a password set in the "KB Power On Password" field, and now you wish to disable the Wake-On-Keyboard (password) function, make sure to set the "Keyboard/Mouse Power On" field to Disabled prior to setting IPI to disabled. You will not be able to boot up the system if you fail to do so.

IP2 (CPU's FSB) - 1-2 On: Auto (default); 2-3 On: 66MHz; 1-2-3 Off: future 100MHz FSB

JP6 (Clear CMOS Data) - 1-2 On: Normal (default); 2-3 On: Clear CMOS Data

If you are using an ATX power supply, make sure to power-off your system and unplug the power cord prior to clearing the CMOS data.