



DIP Switch Settings of the Processors

Processor		Frequency Ratio	SW1	Processor		Frequency Ratio	SW1
66MHz	100MHz			66MHz	100MHz		
300MHz	Future processor	4.5x		433MHz	Future processor	6.5x	
333MHz	Future processor	5x		466MHz	Future processor	7x	
366MHz	Future processor	5.5x		Future processor	Future processor	7.5x	
400MHz	Future processor	6x		Future processor	Future processor	8x	



In the example above:

- Switch 1: Off
- Switch 2: On
- Switch 3: Off
- Switch 4: On

Note: 1. The voltage regulator will automatically be set according to the voltage of the processor.
 2. You cannot overclock an Intel Celeron™ processor (PPGA for Socket 370) because its frequency ratio has been fixed by the manufacturer. The table above is for factory use only;

JP1 (Wake-On-Keyboard/Mouse) - 2-3 On: Enable; 1-2 On: Disable (default)
 By default, JP1 is disabled. Make sure "Keyboard/Mouse Power On" in the Integrated Peripherals setup of the Award BIOS is also disabled. If JP1 was previously enabled with a password set in the "KB Power On Password" field, and now you wish to disable the Wake-On-Keyboard (password) function, make sure to set the "Keyboard/Mouse Power On" field to Disabled prior to setting JP1 to disabled. You will not be able to boot up the system if you fail to do so.

JP2 (CPU's FSB) - 1-2 On: Auto (default); 2-3 On: 66MHz; 1-2-3 Off: future 100MHz FSB

JP6 (Clear CMOS Data) - 1-2 On: Normal (default); 2-3 On: Clear CMOS Data
 If you are using an ATX power supply, make sure to power-off your system and unplug the power cord prior to clearing the CMOS data.